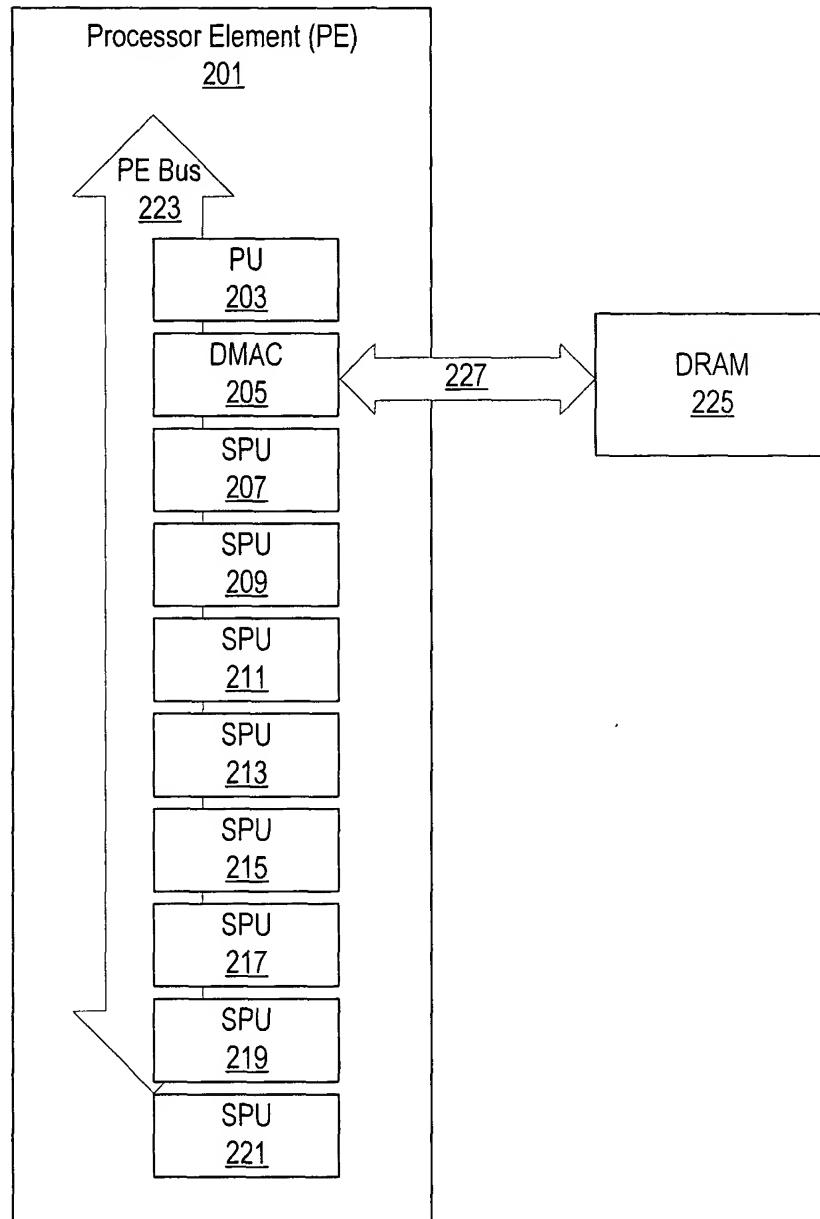
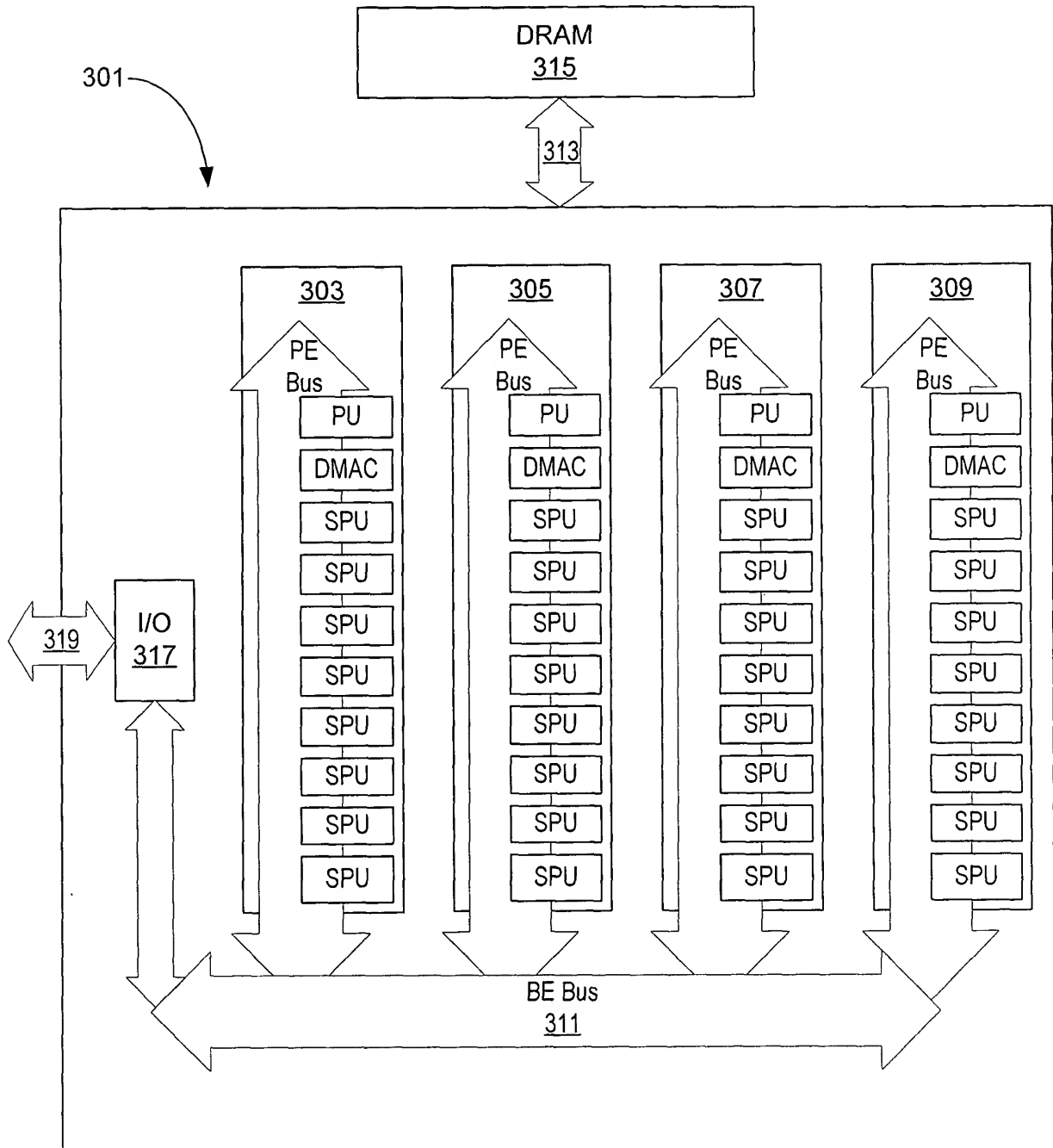
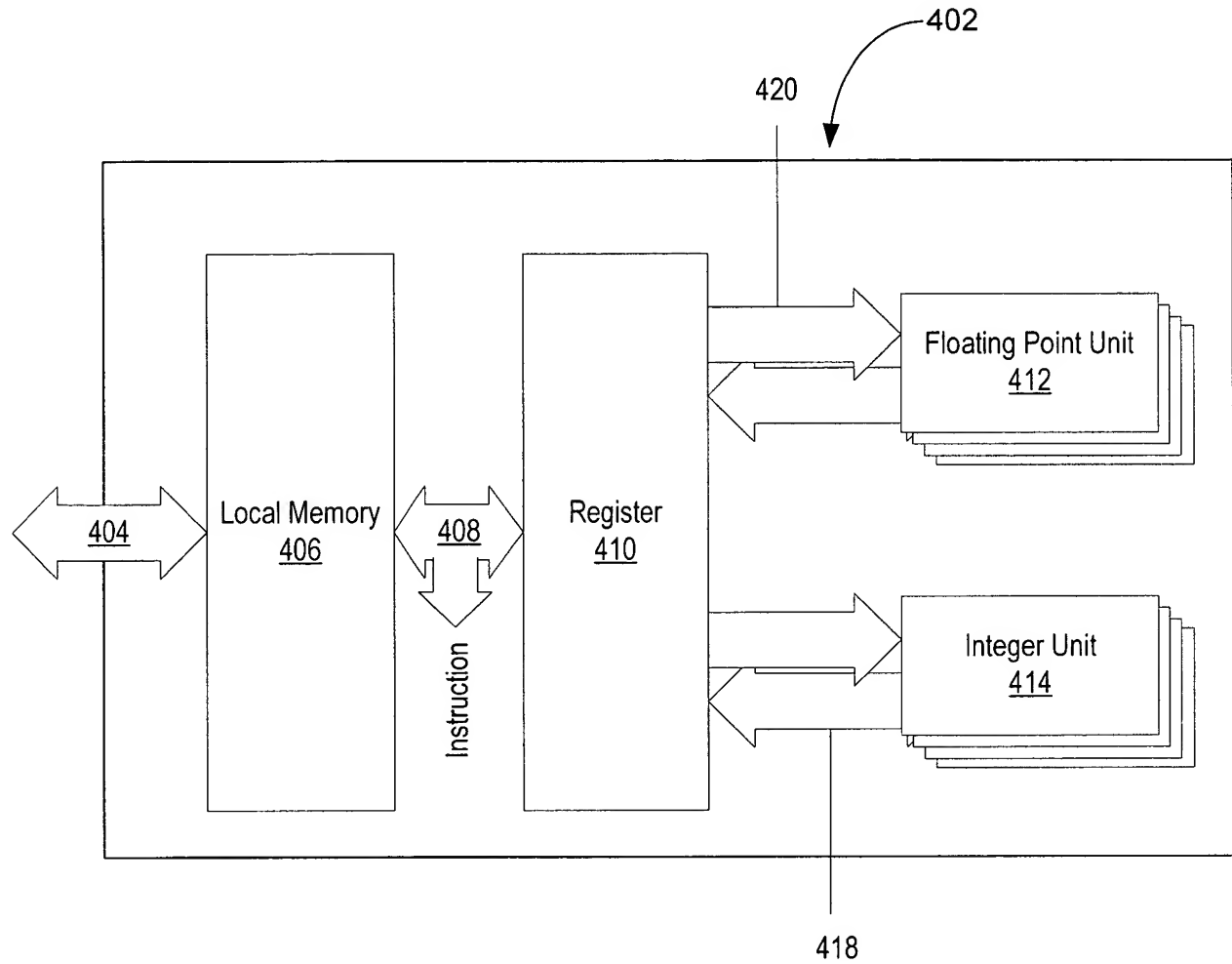
**Figure 1**

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**Figure 2**

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**Figure 3**

**Figure 4**

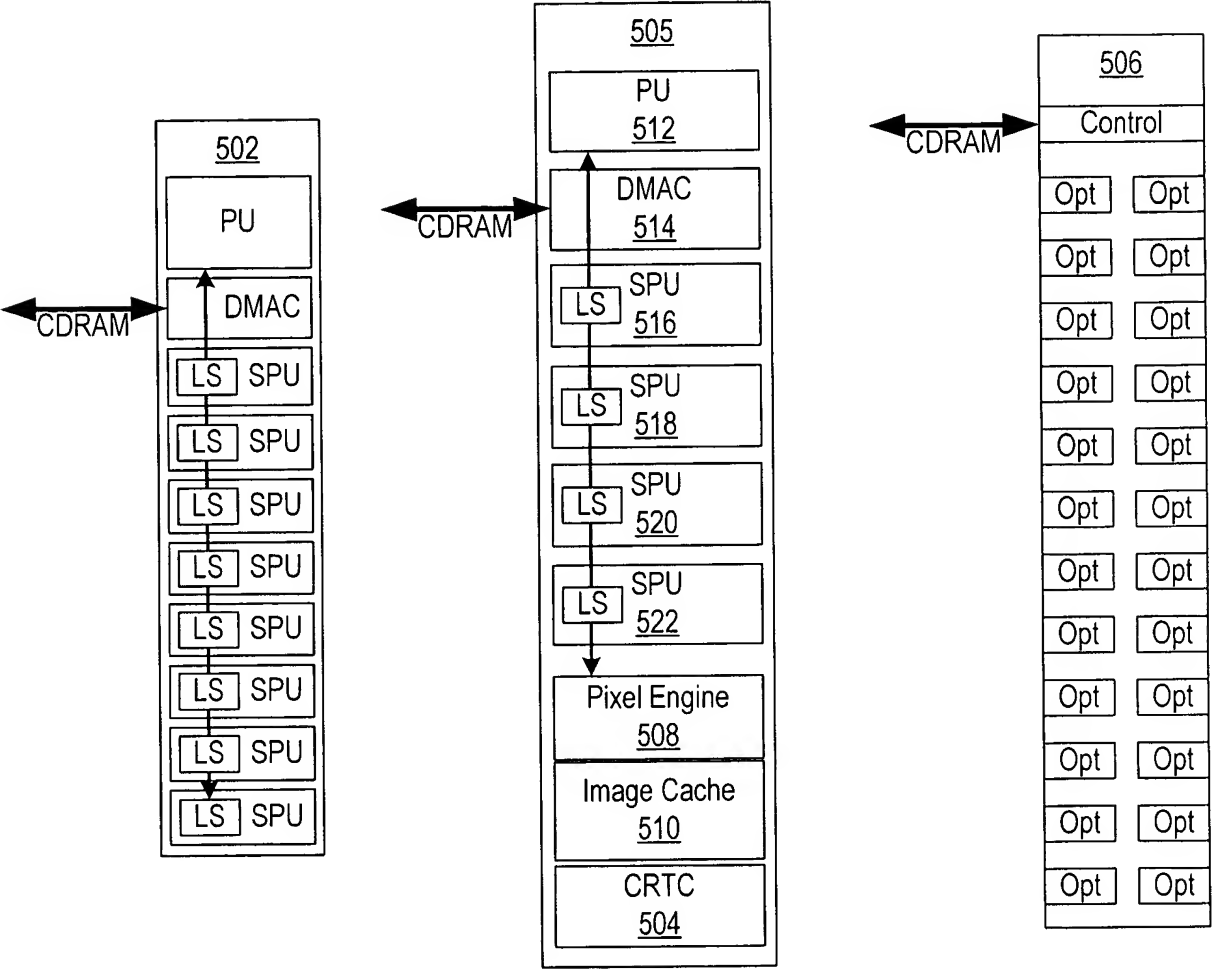
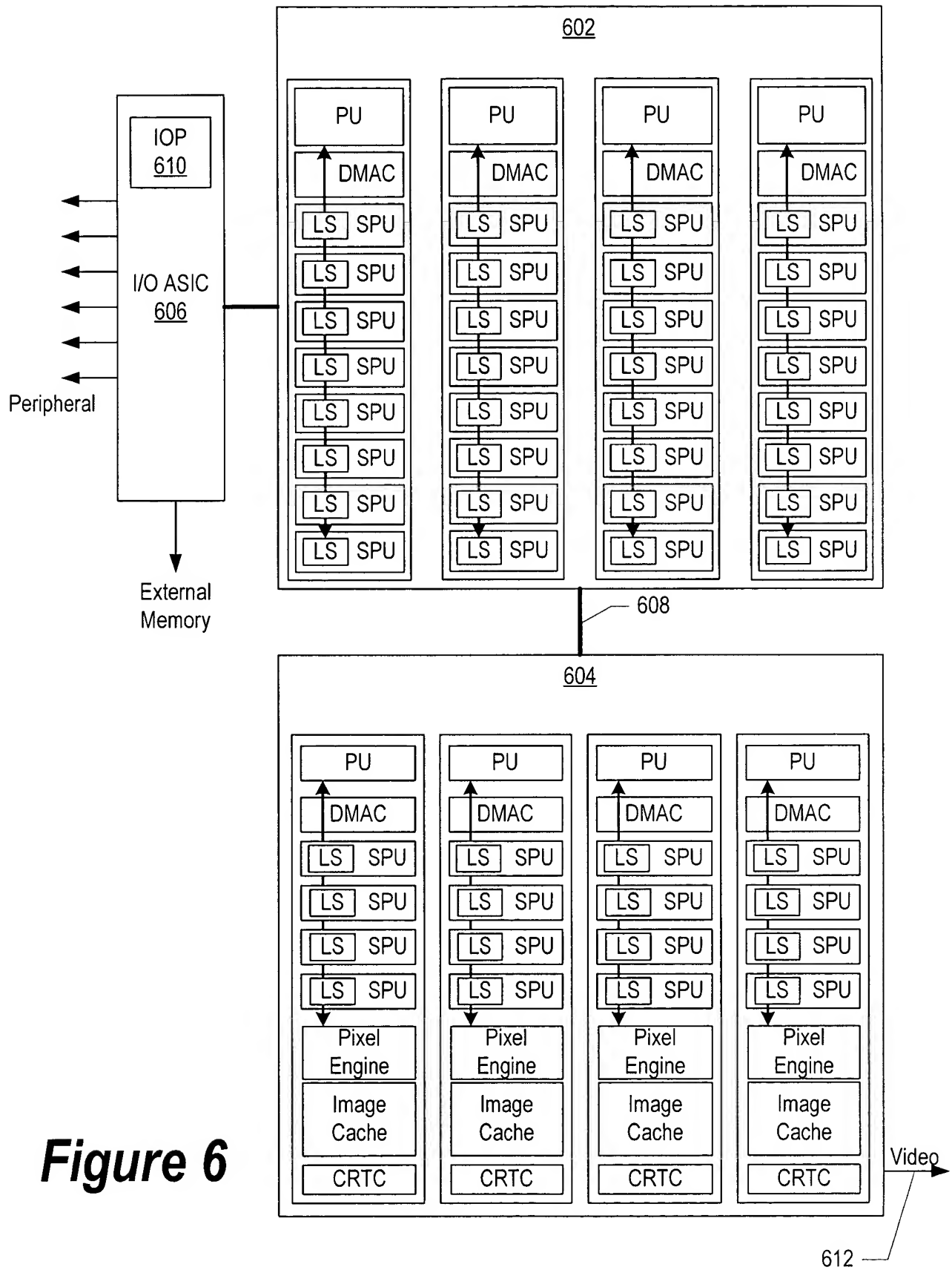


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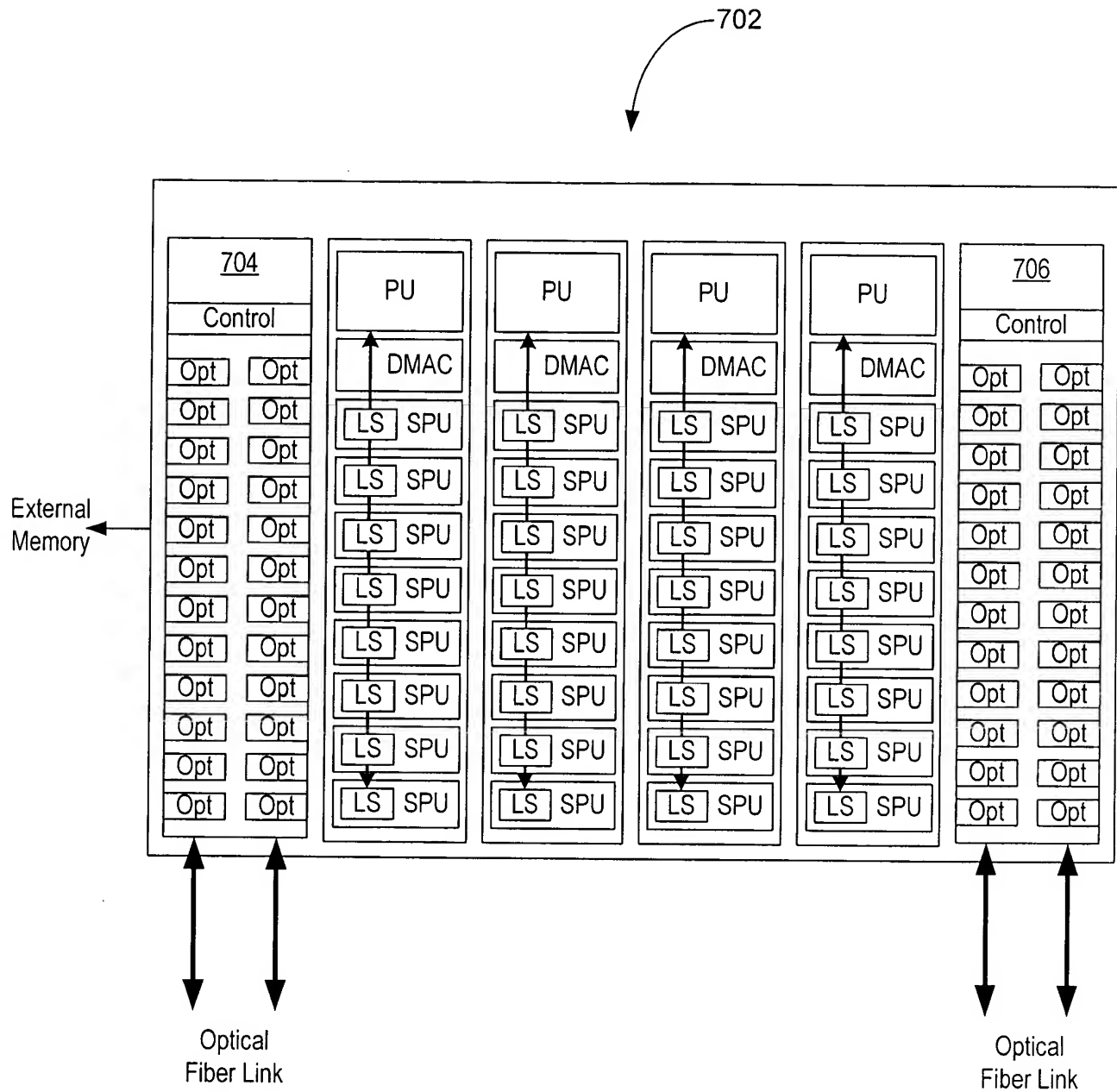
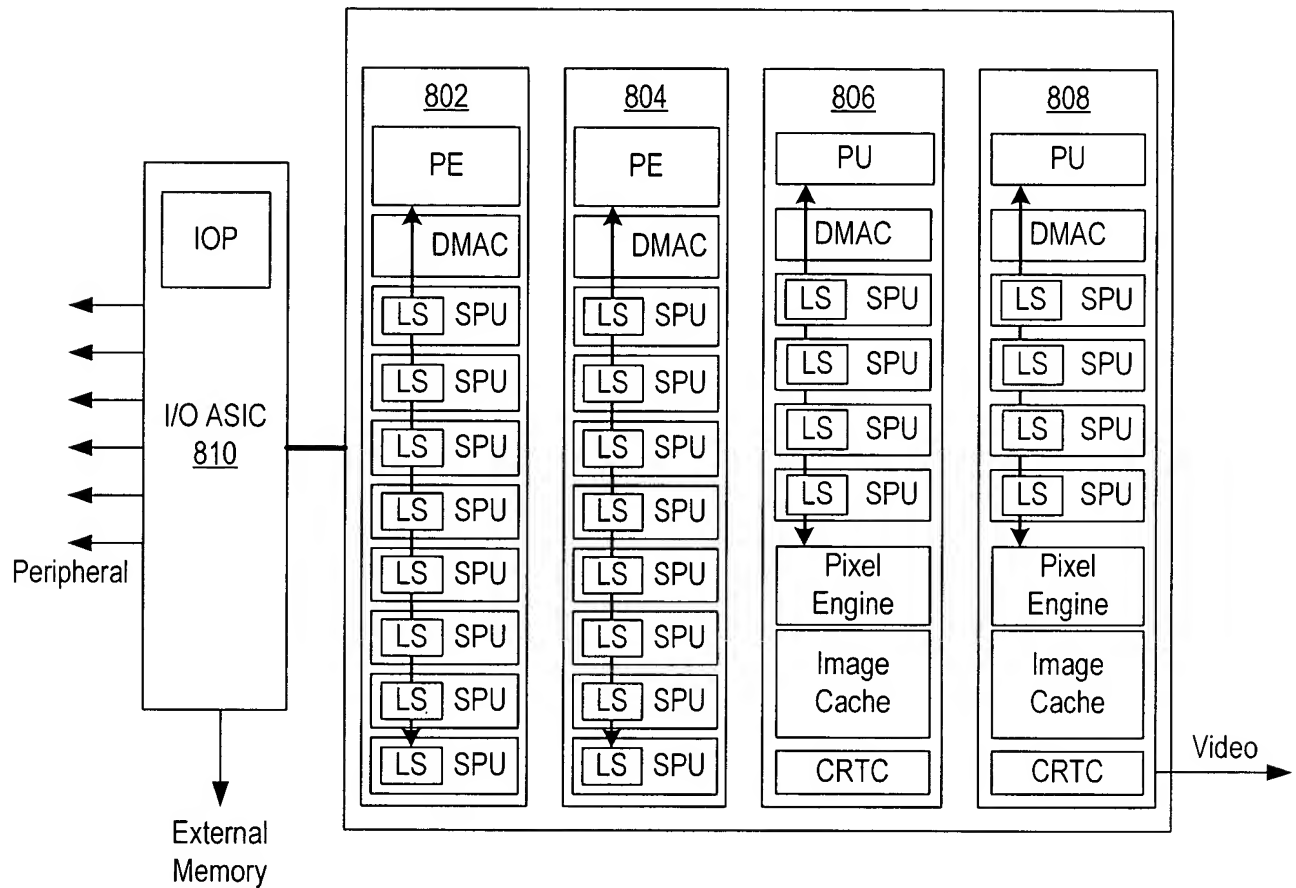
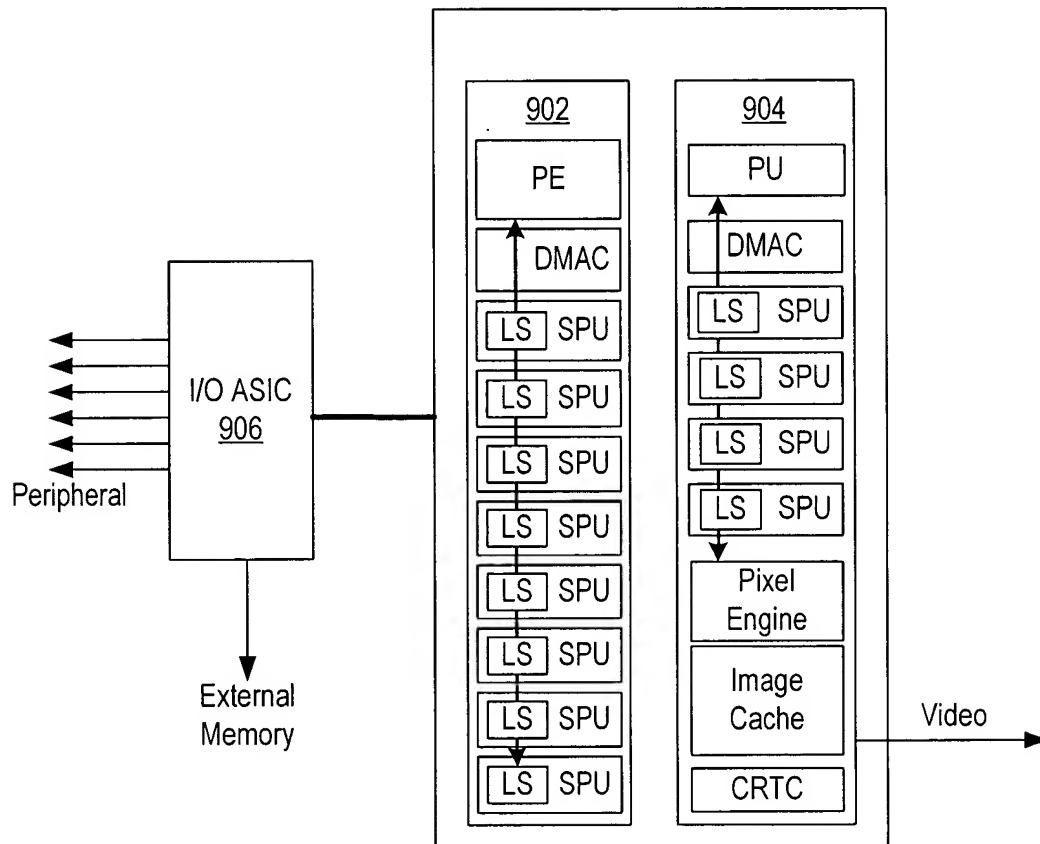
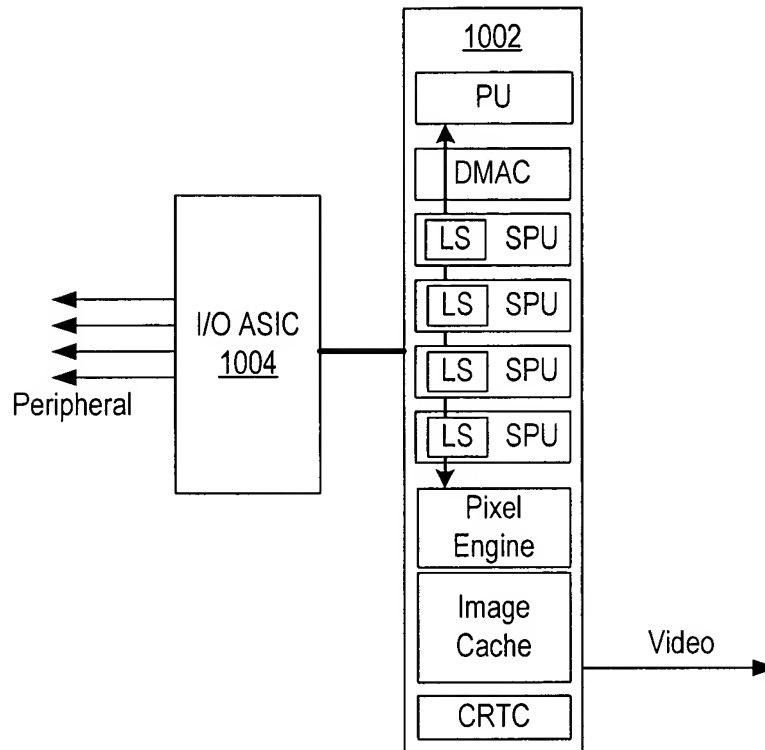


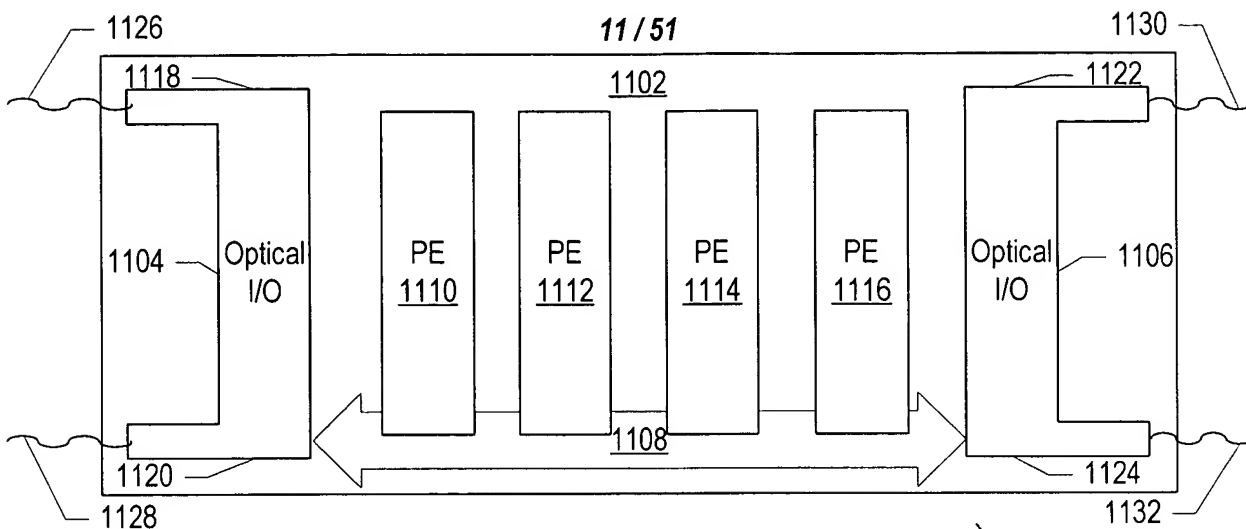
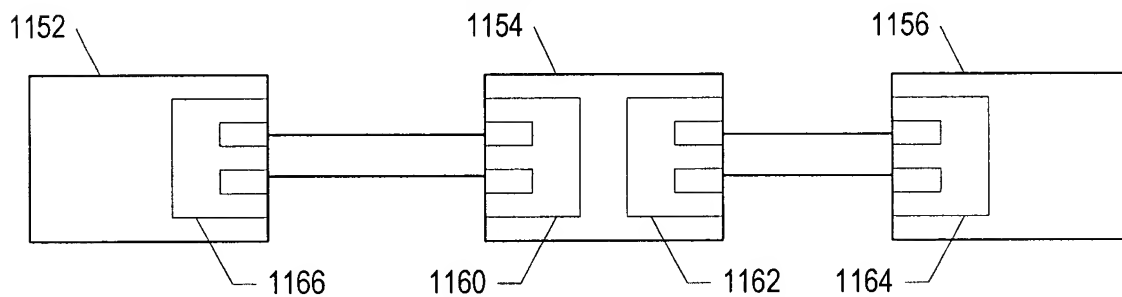
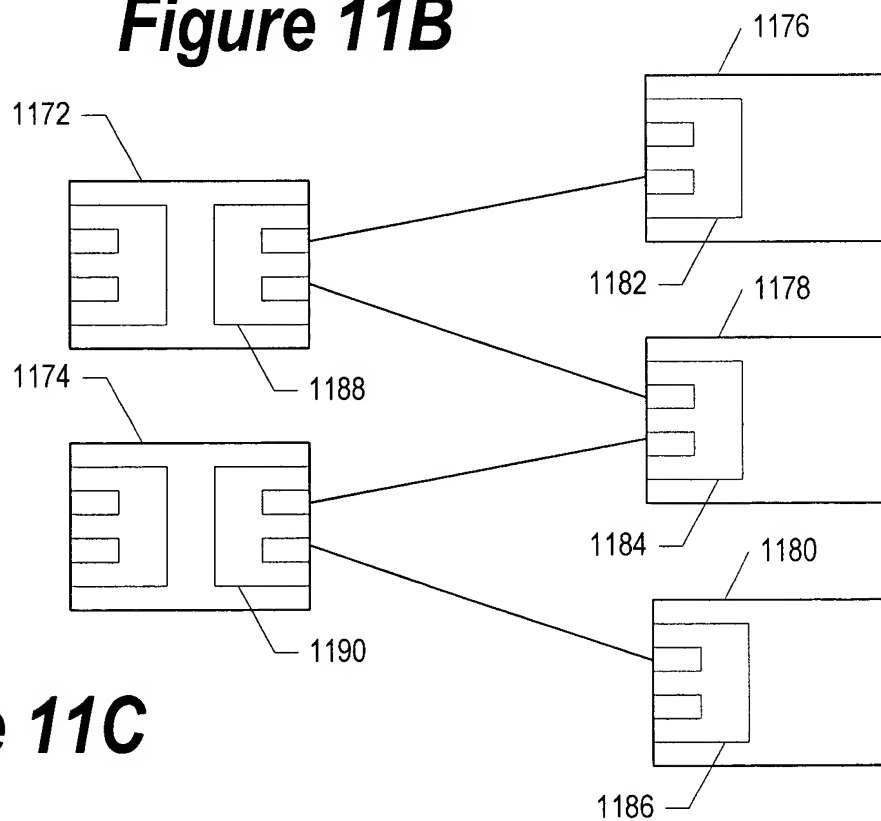
Figure 7

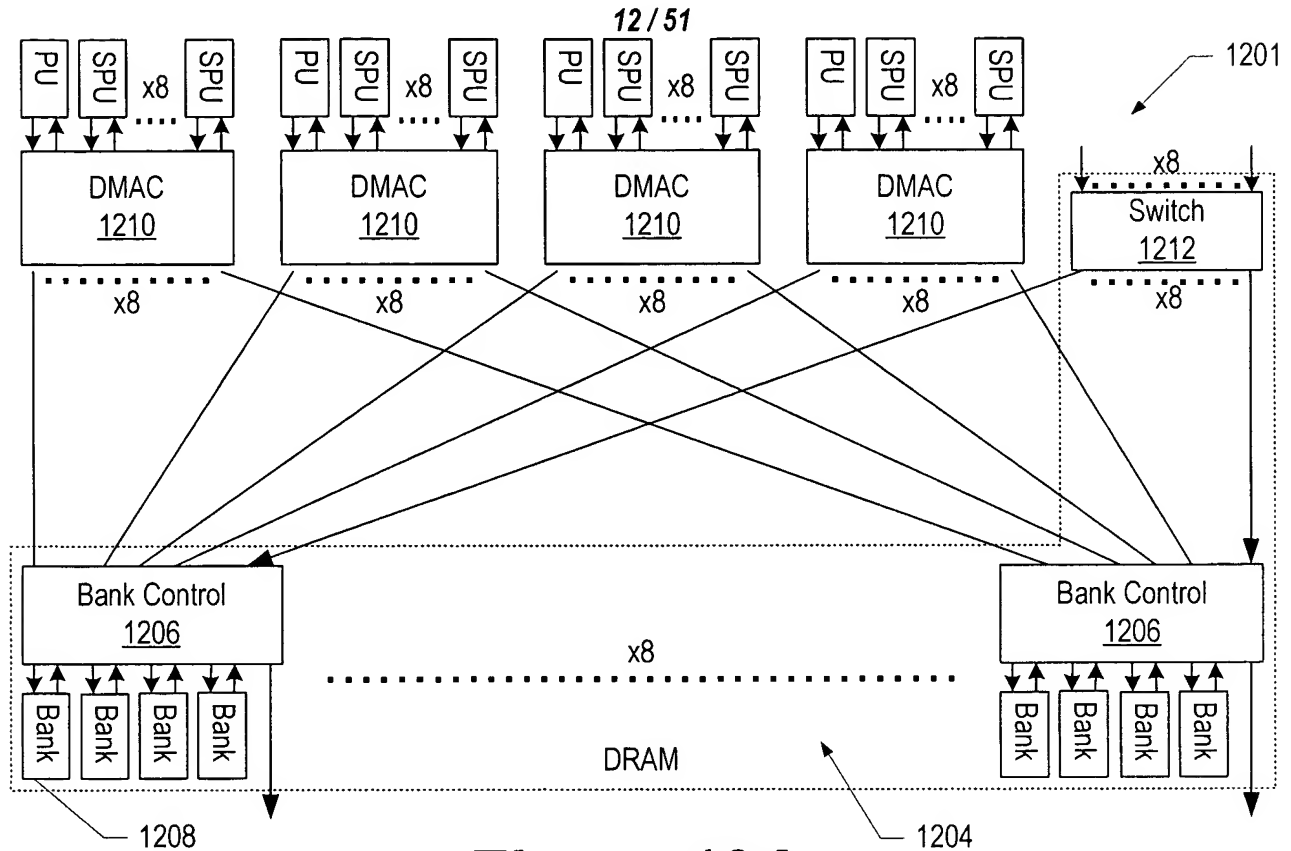
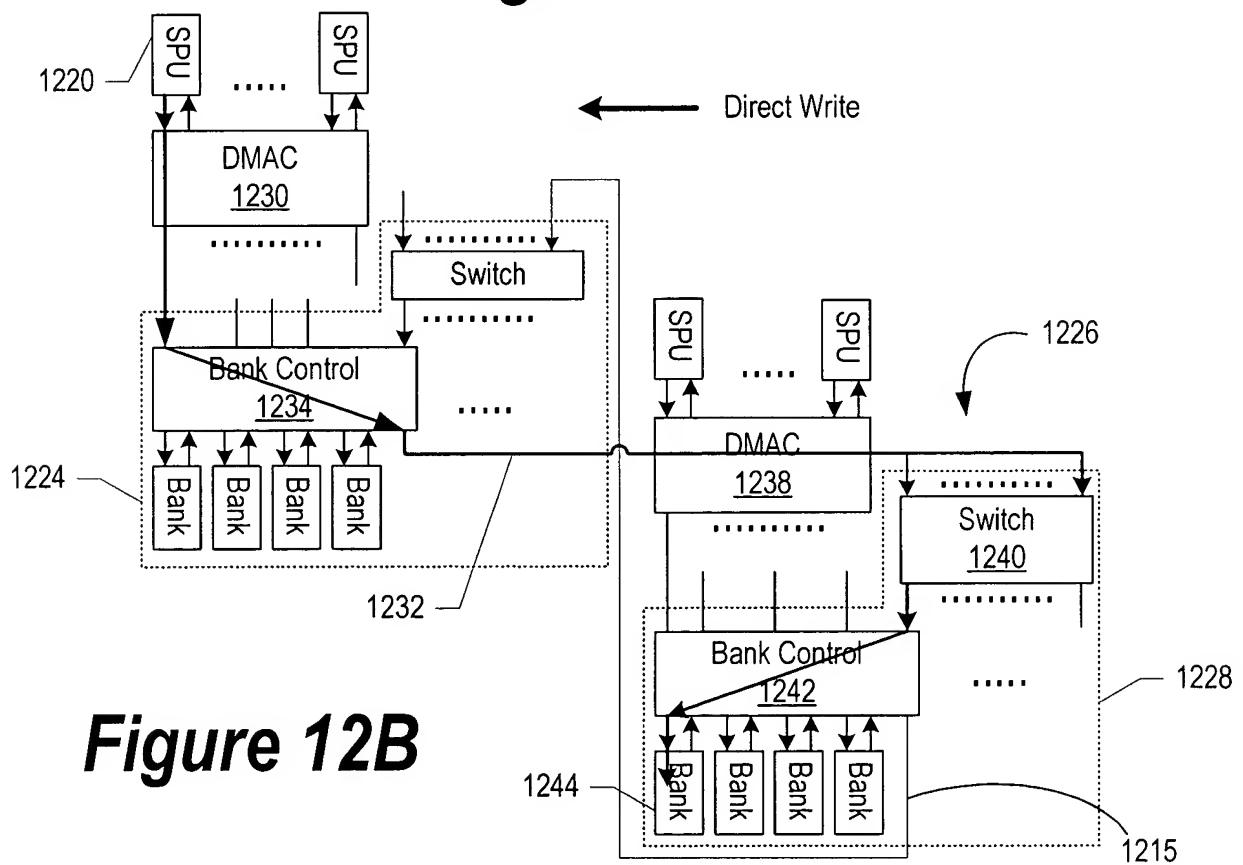
**Figure 8**

**Figure 9**

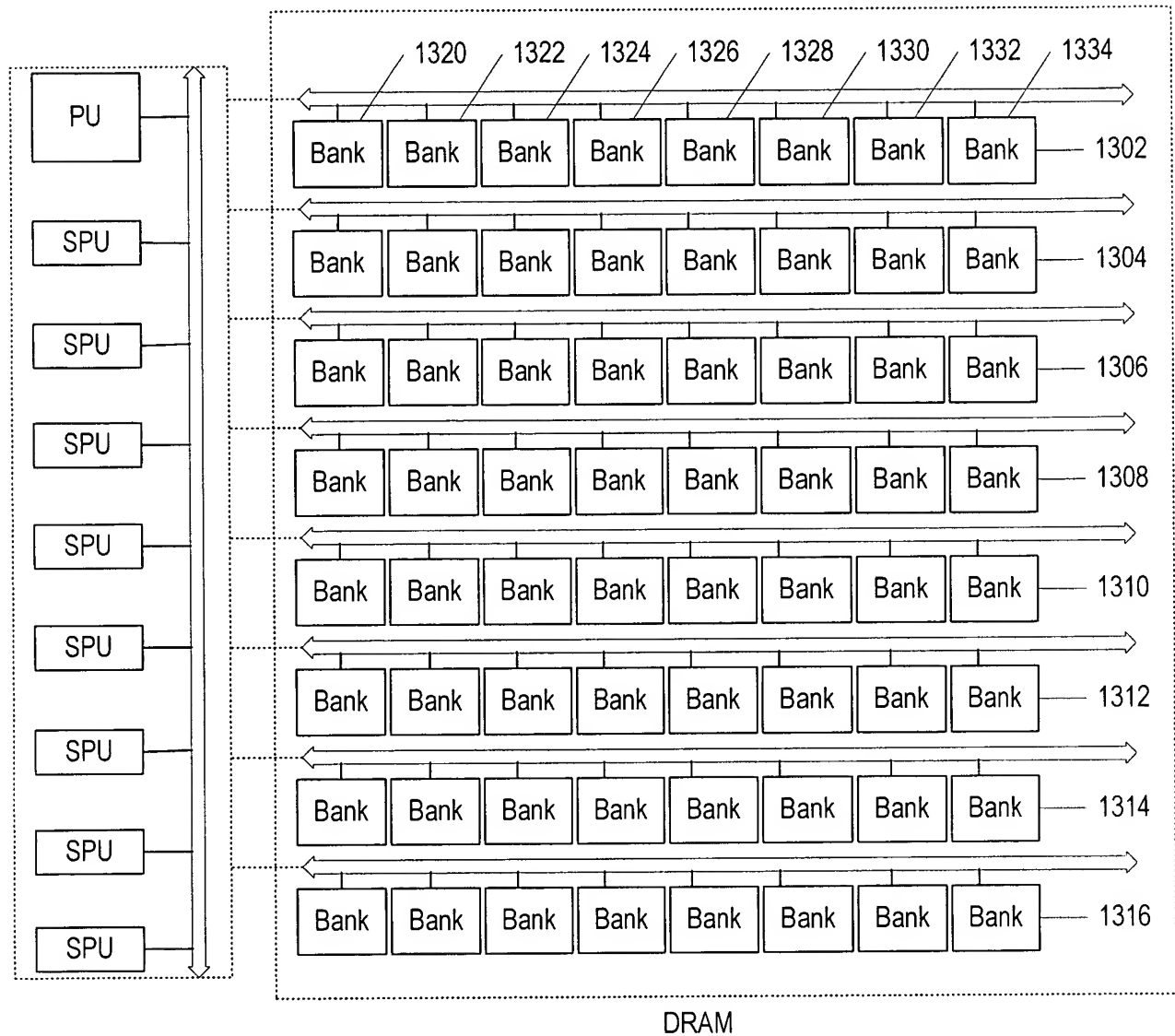
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**Figure 10**

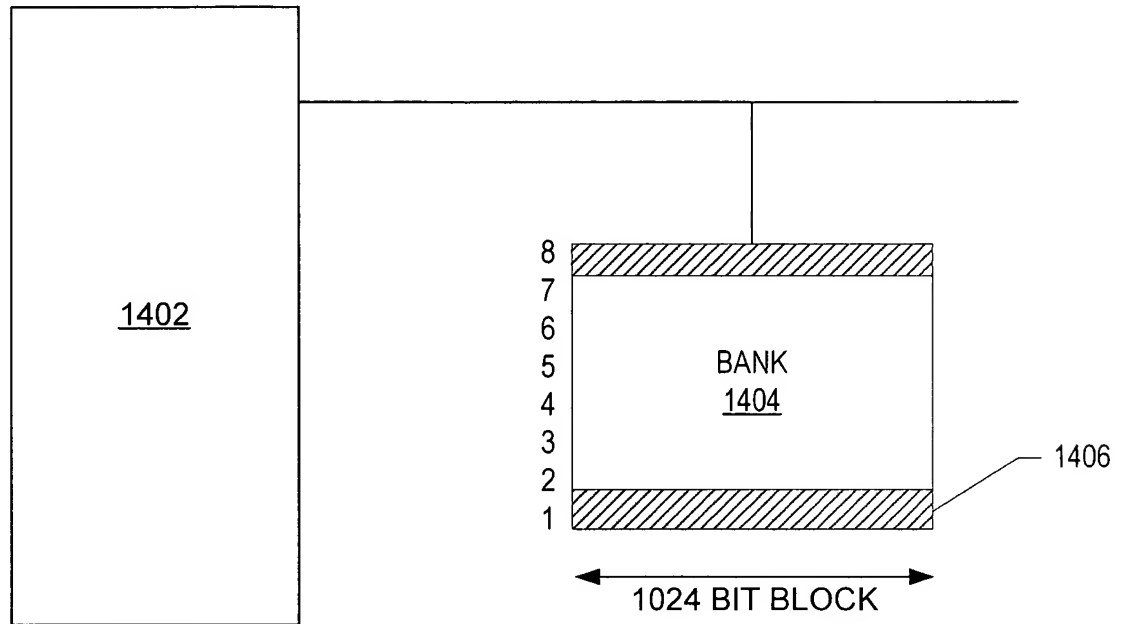
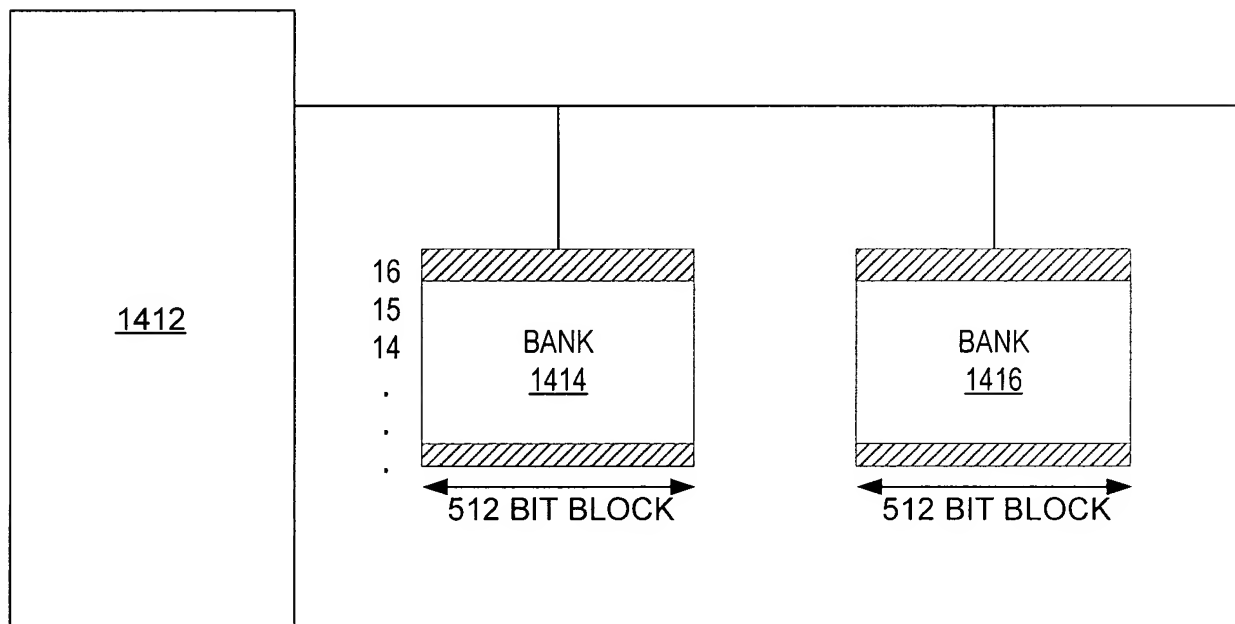
**Figure 11A****Figure 11B****Figure 11C**

**Figure 12A****Figure 12B**

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**Figure 13**

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**Figure 14A****Figure 14B**

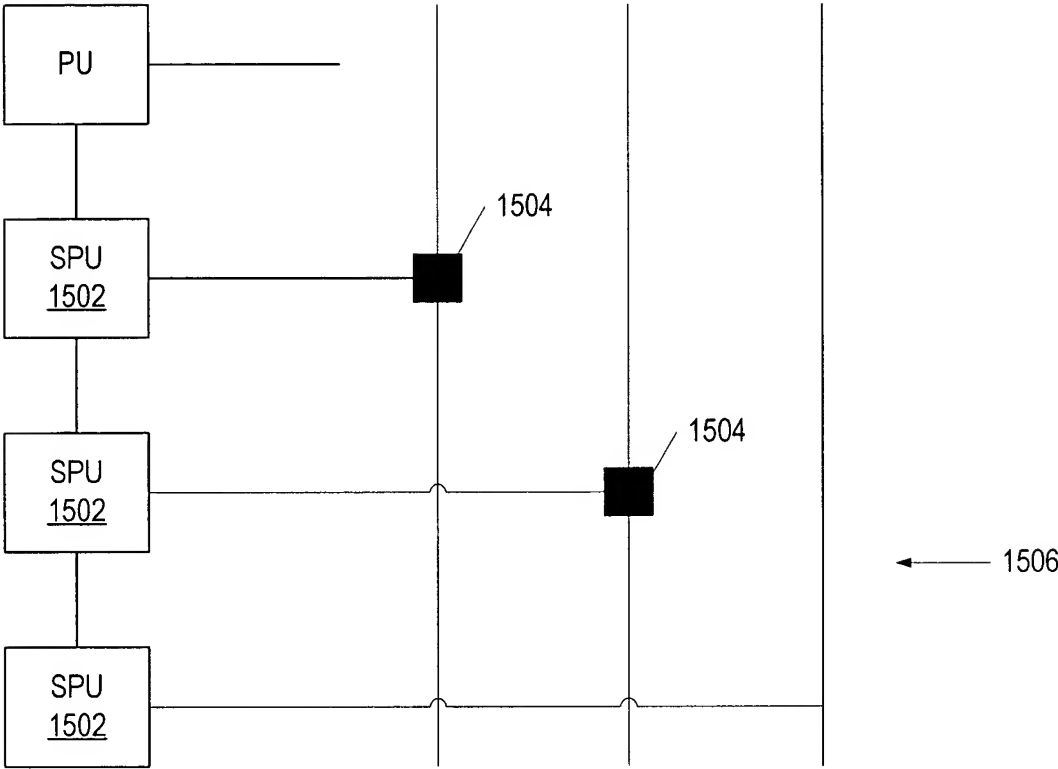
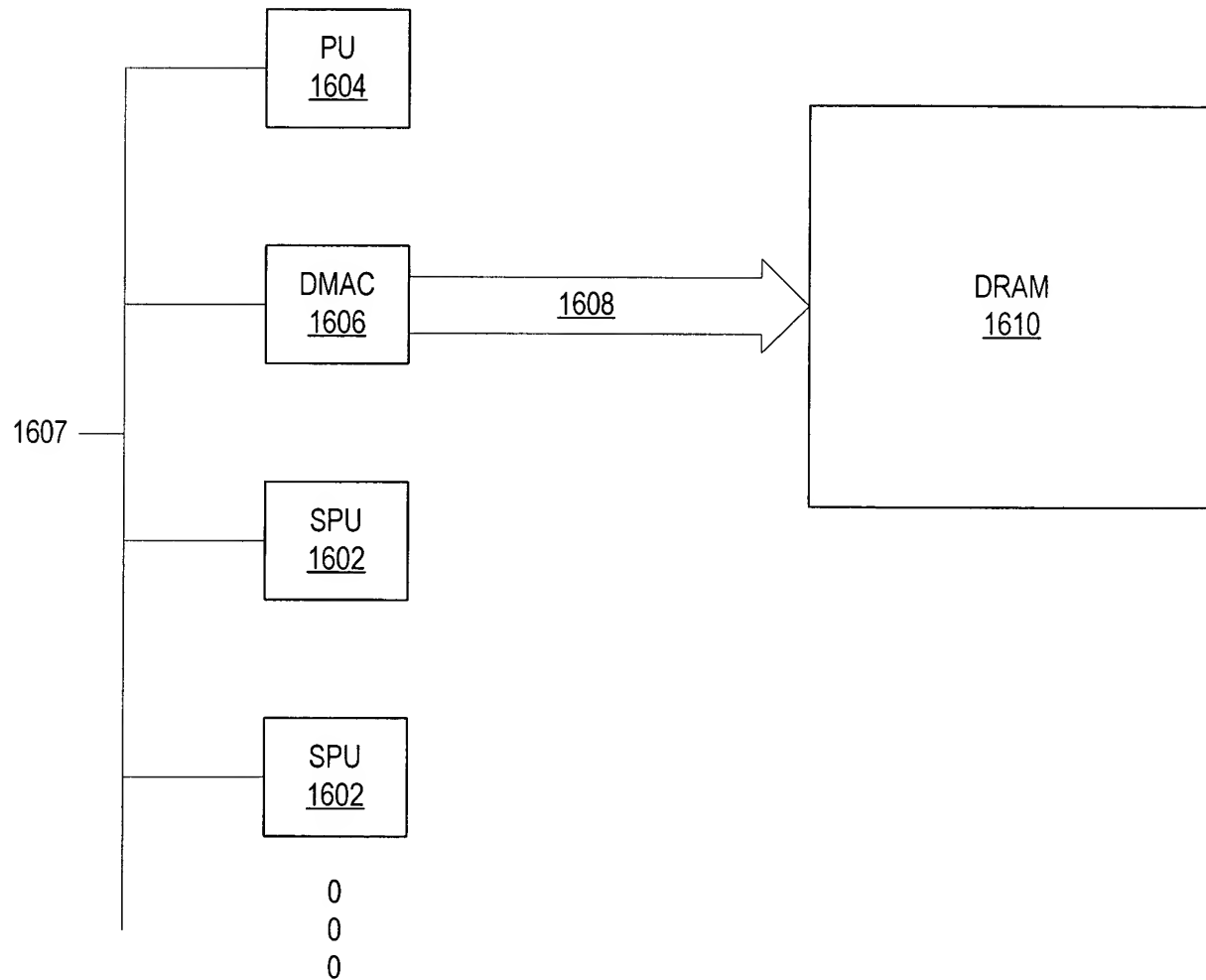
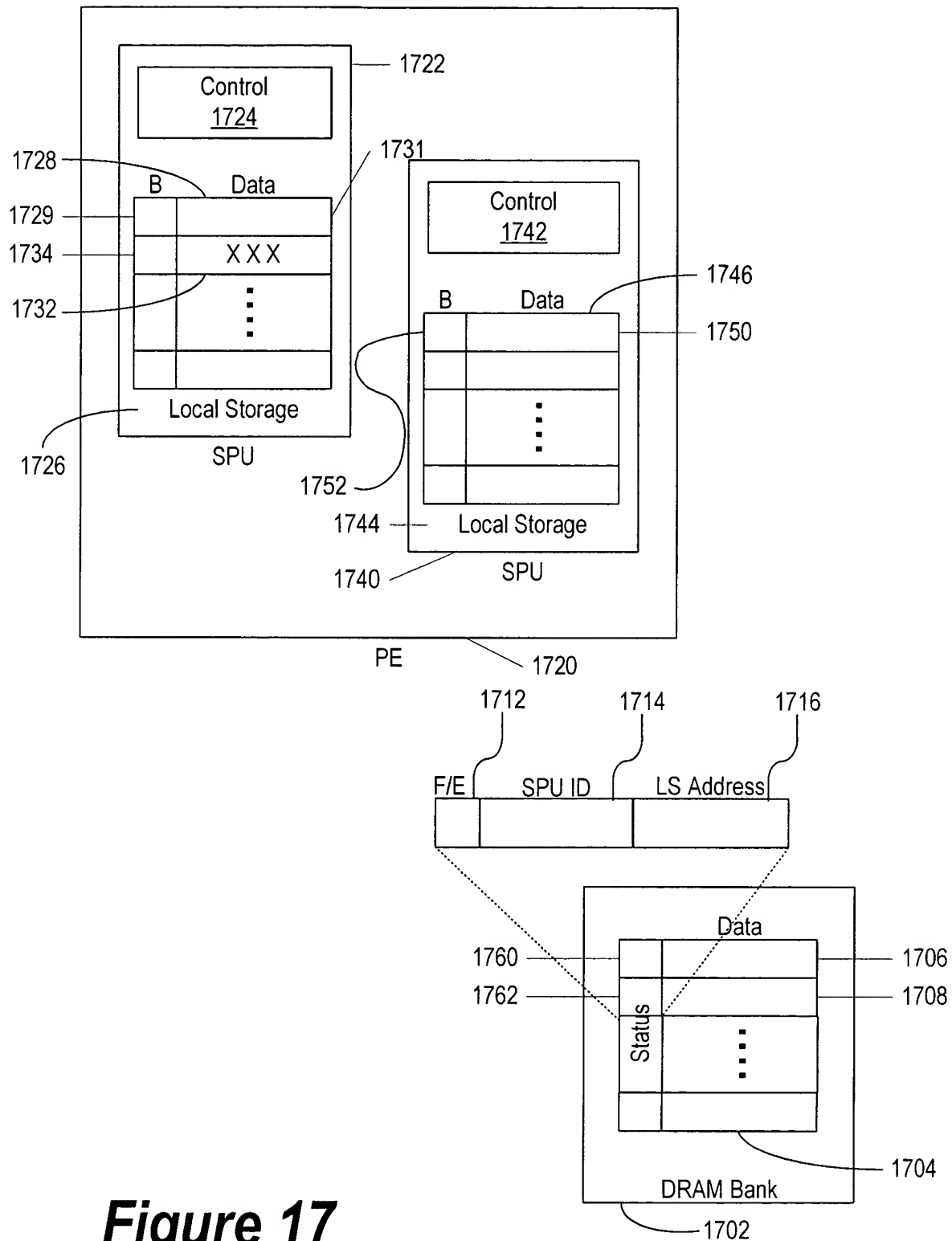


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**Figure 16**

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**Figure 17**

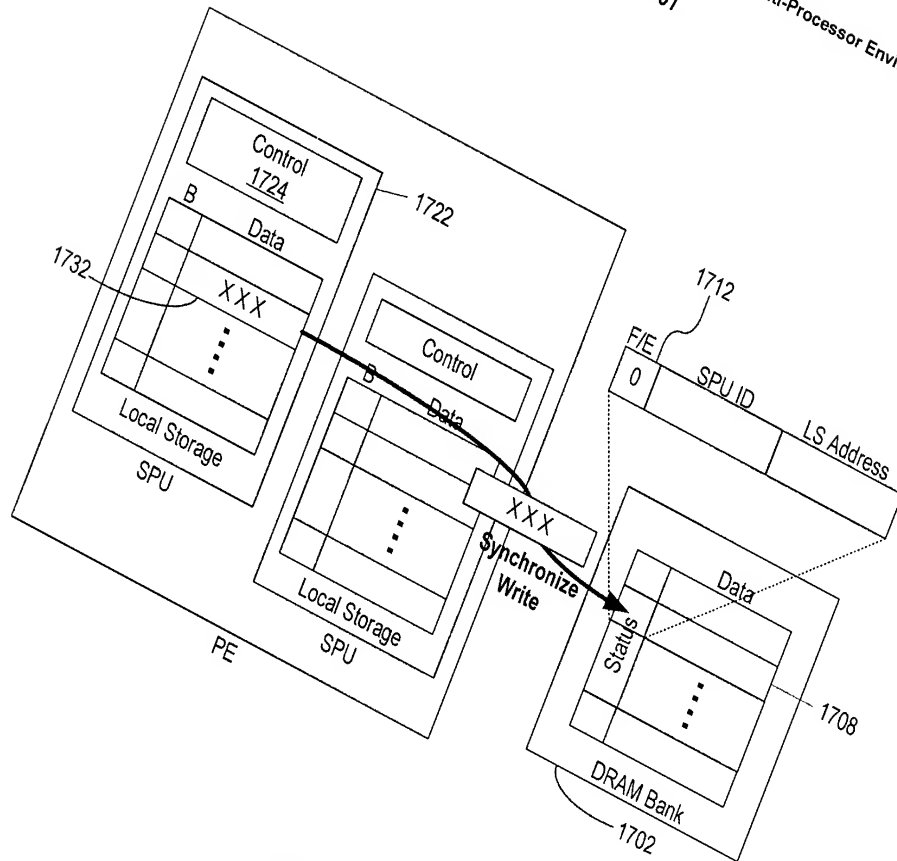


Figure 18

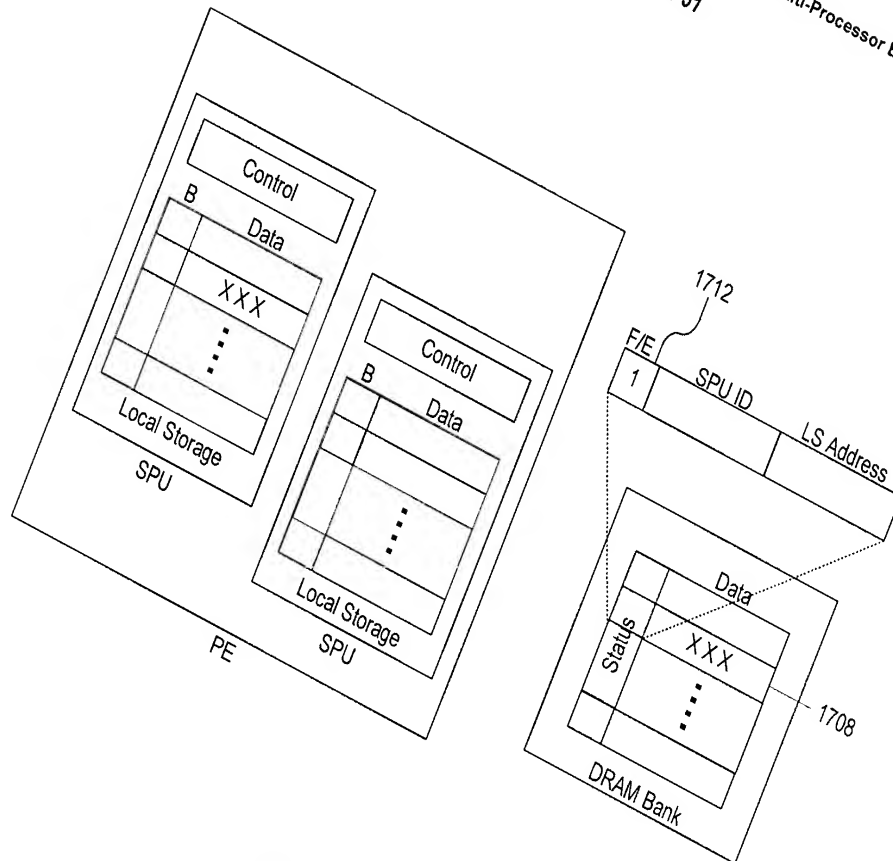
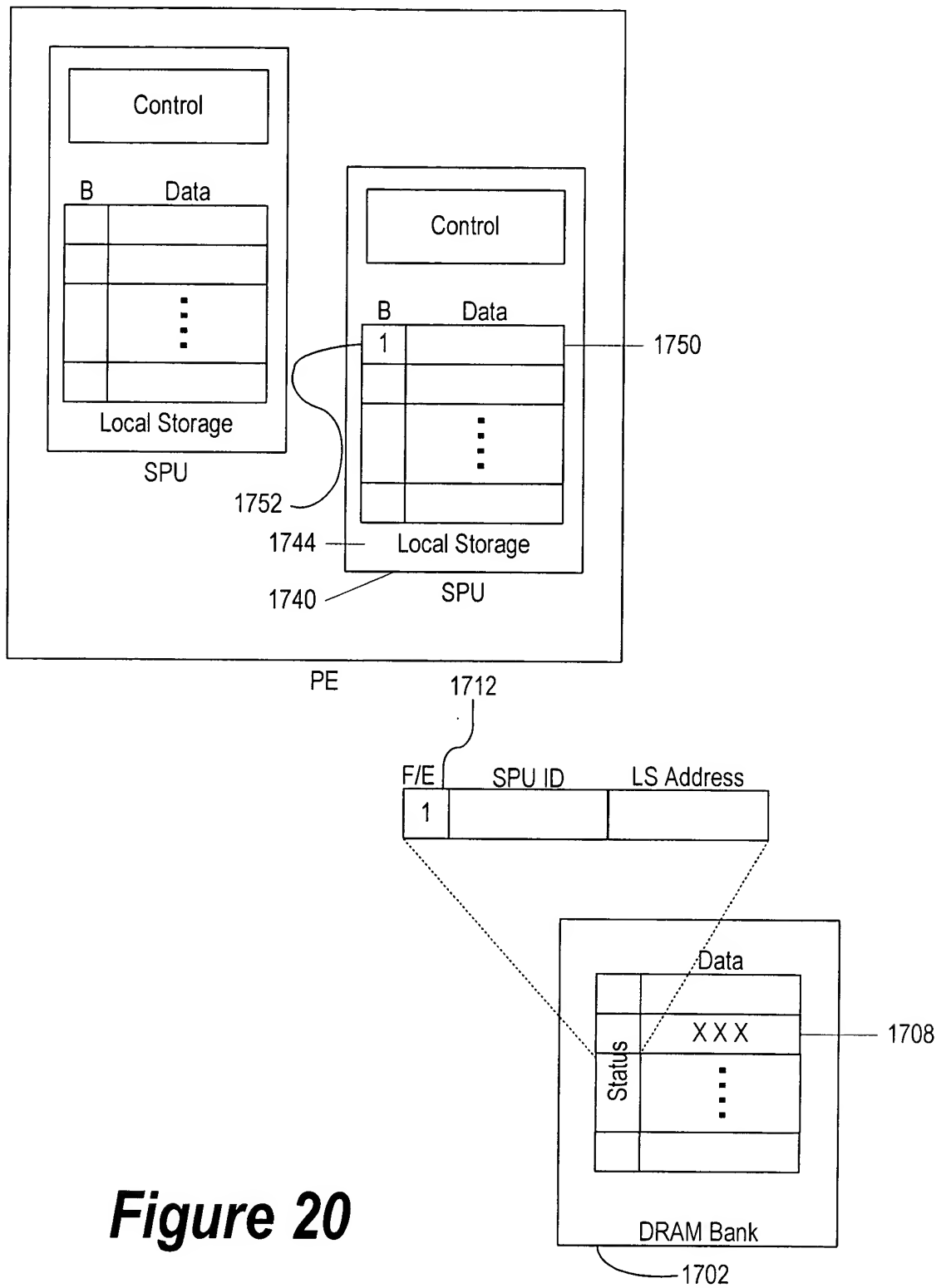


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**Figure 20**

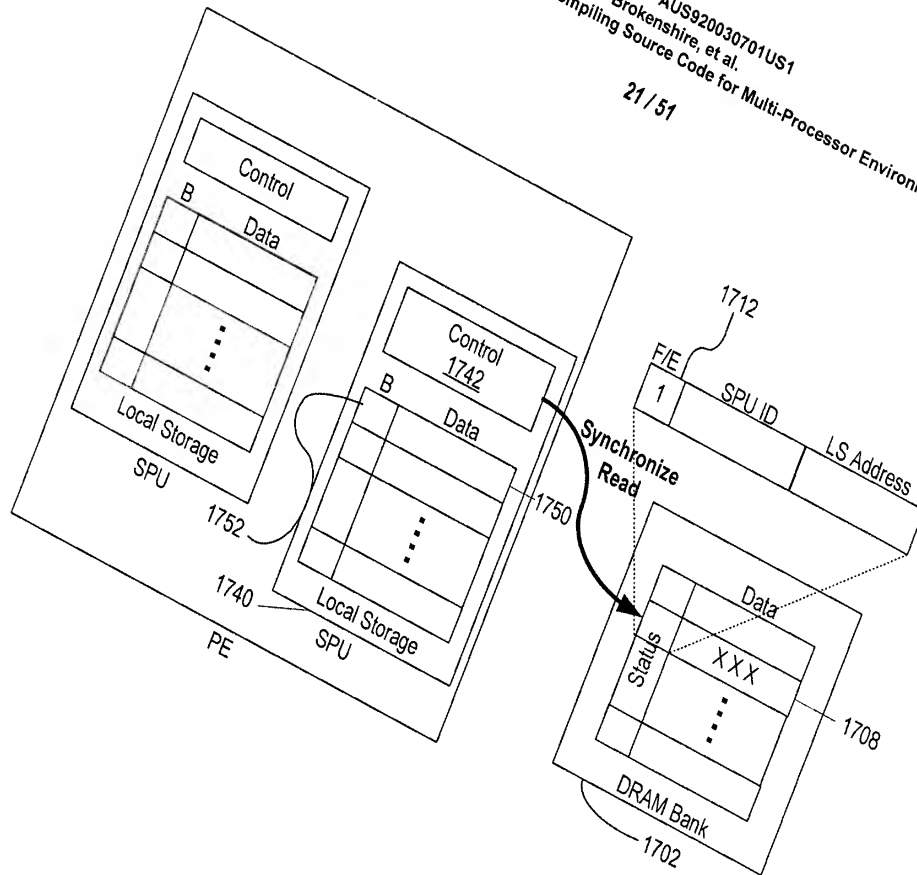
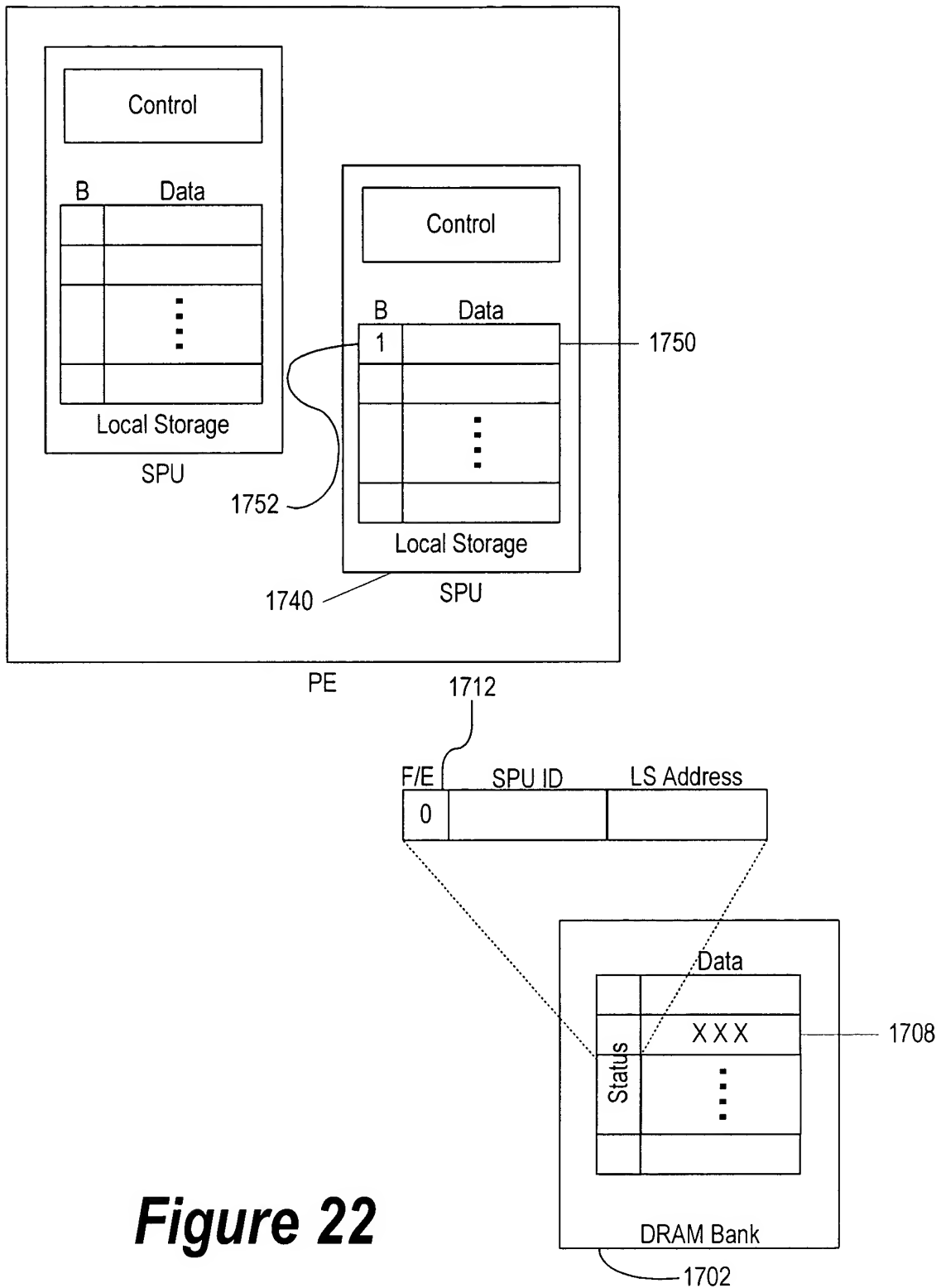


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**Figure 22**

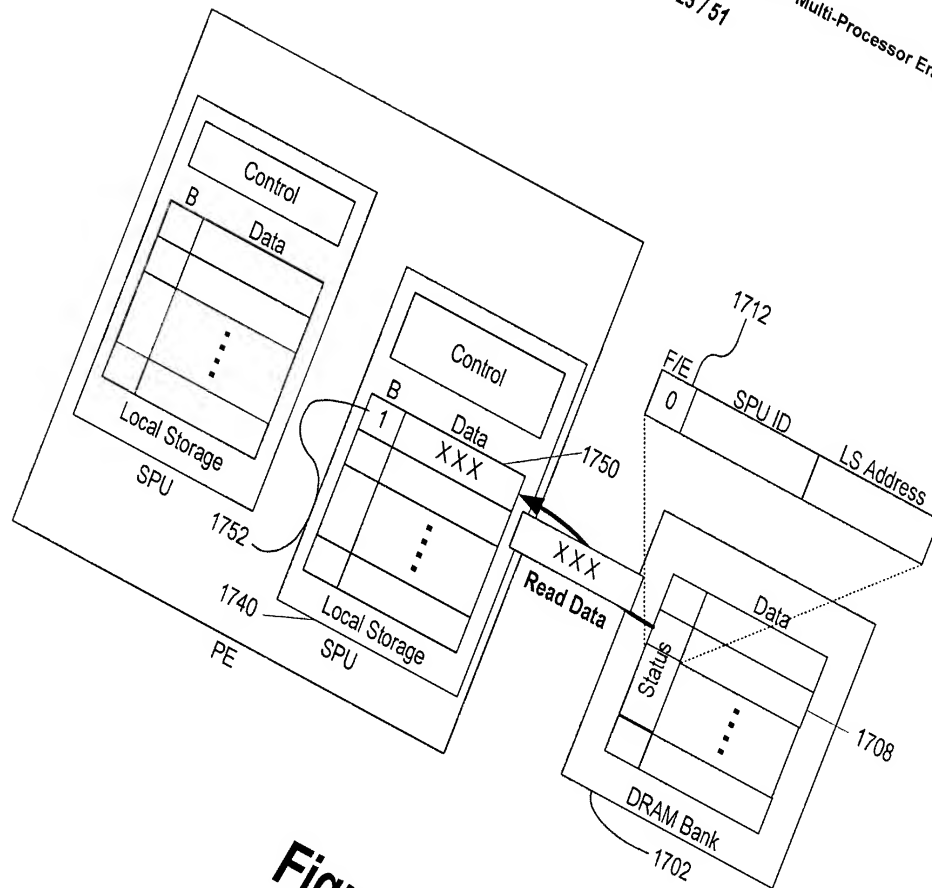


Figure 23

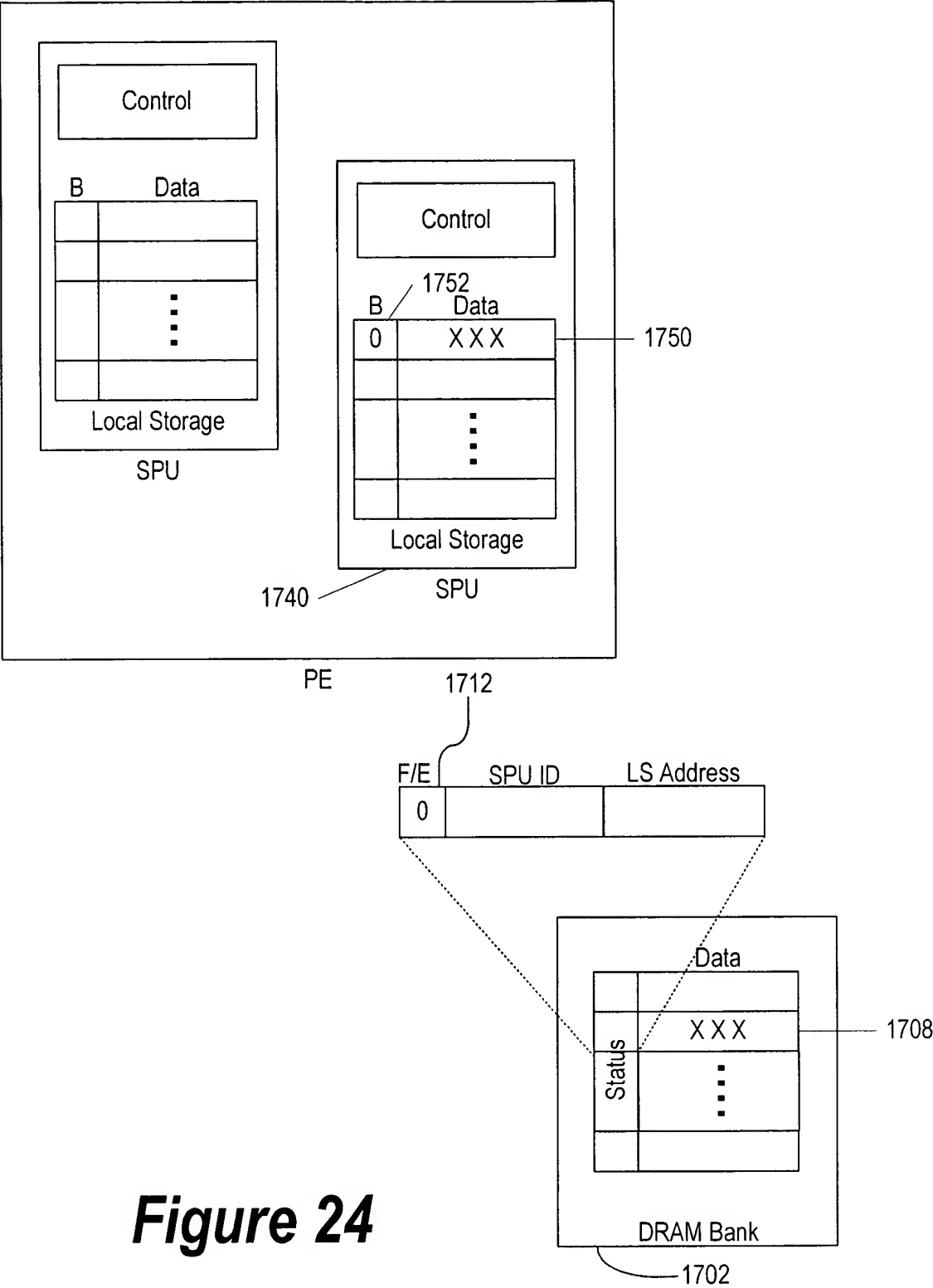
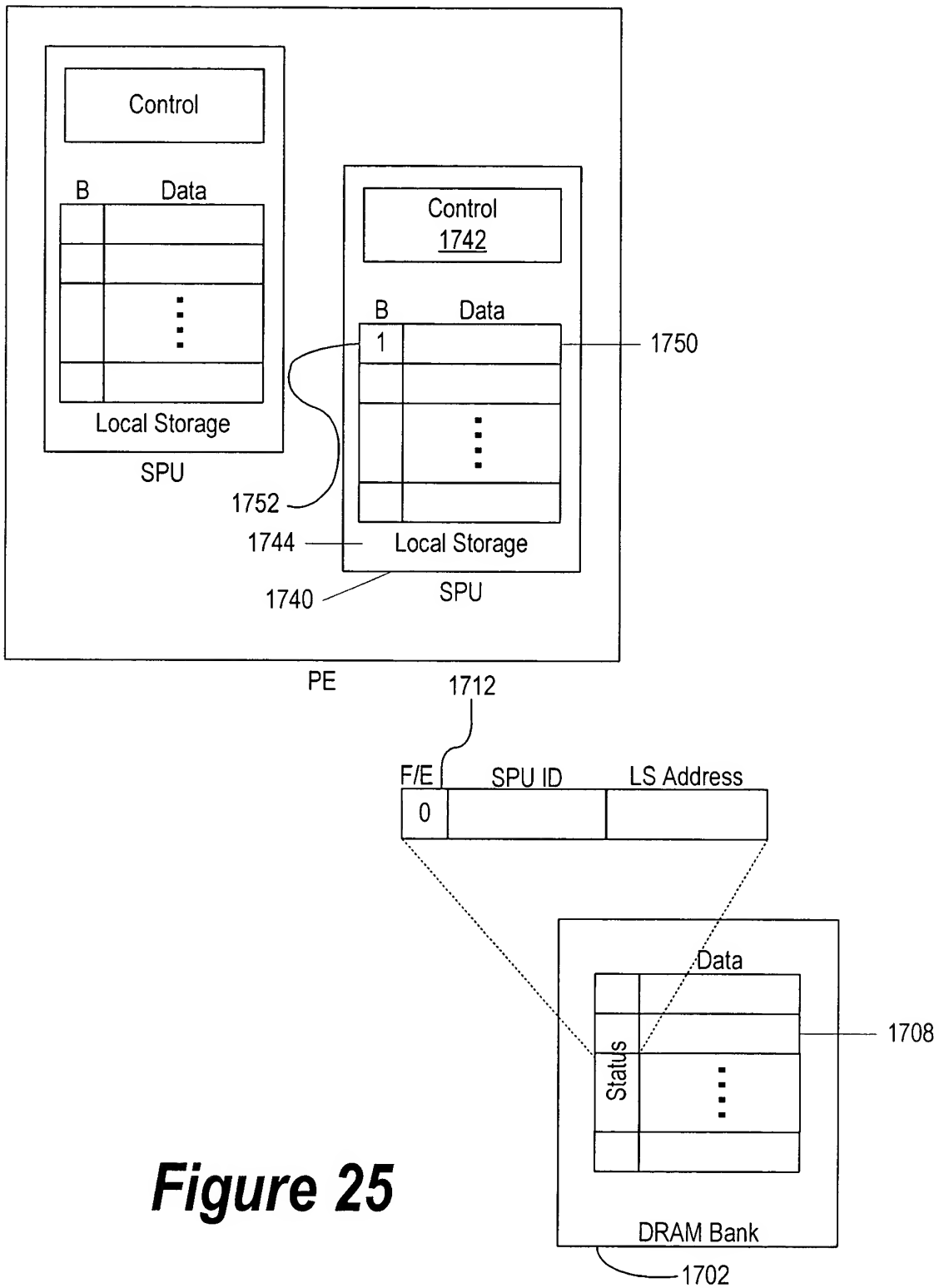


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**Figure 25**

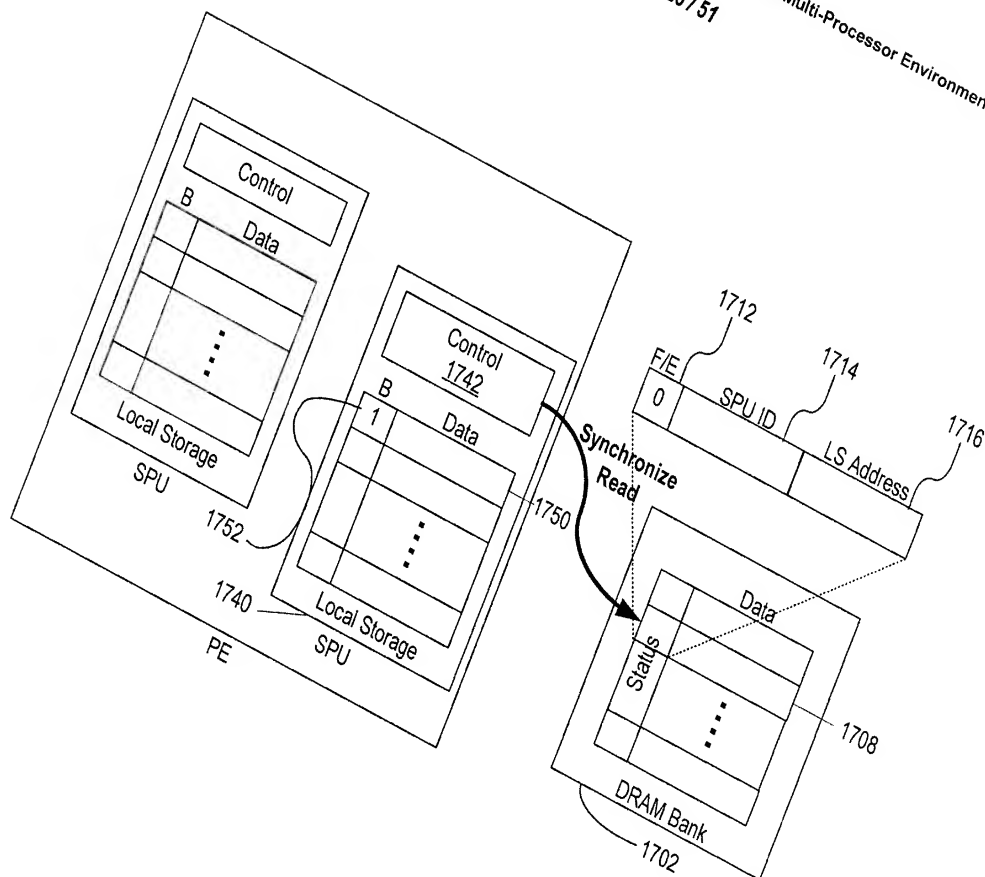


Figure 26

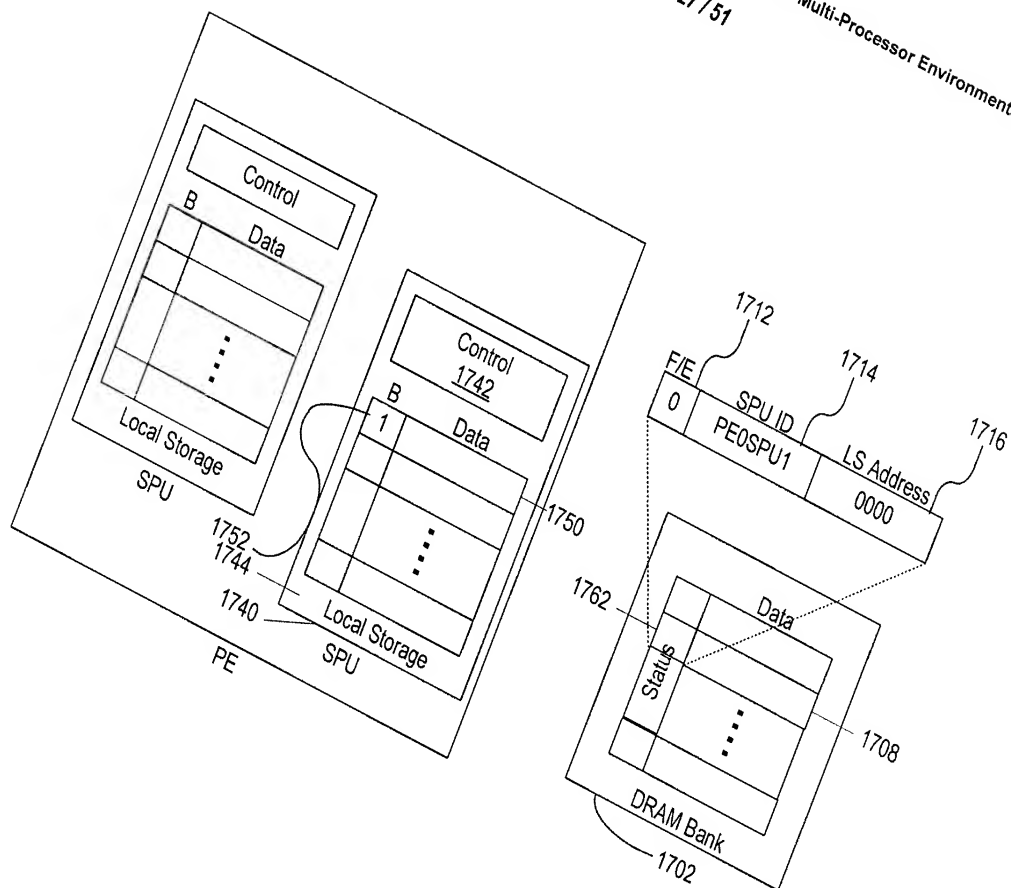


Figure 27

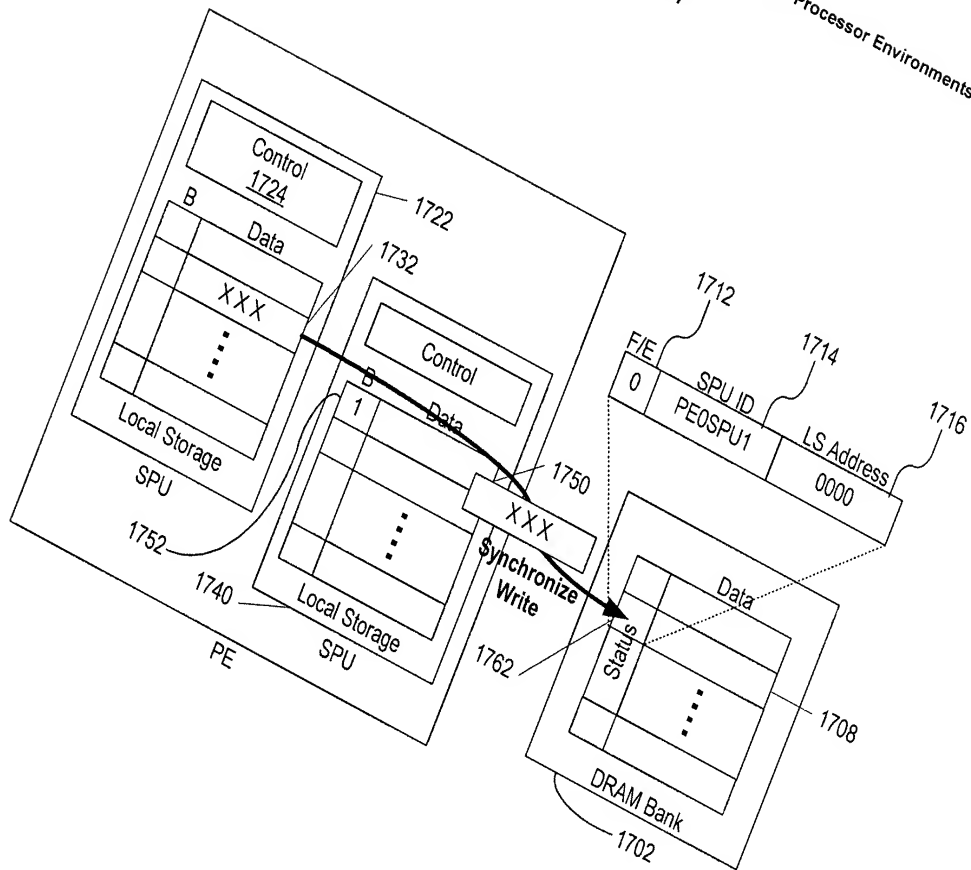


Figure 28

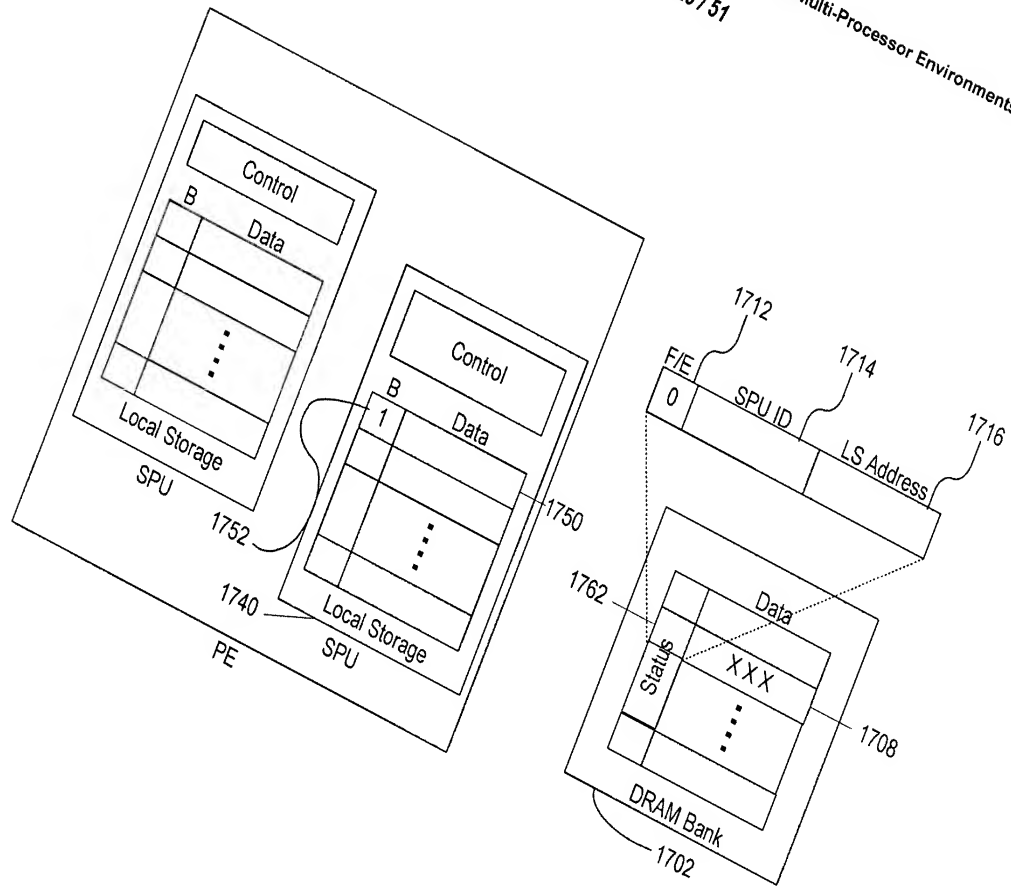


Figure 29

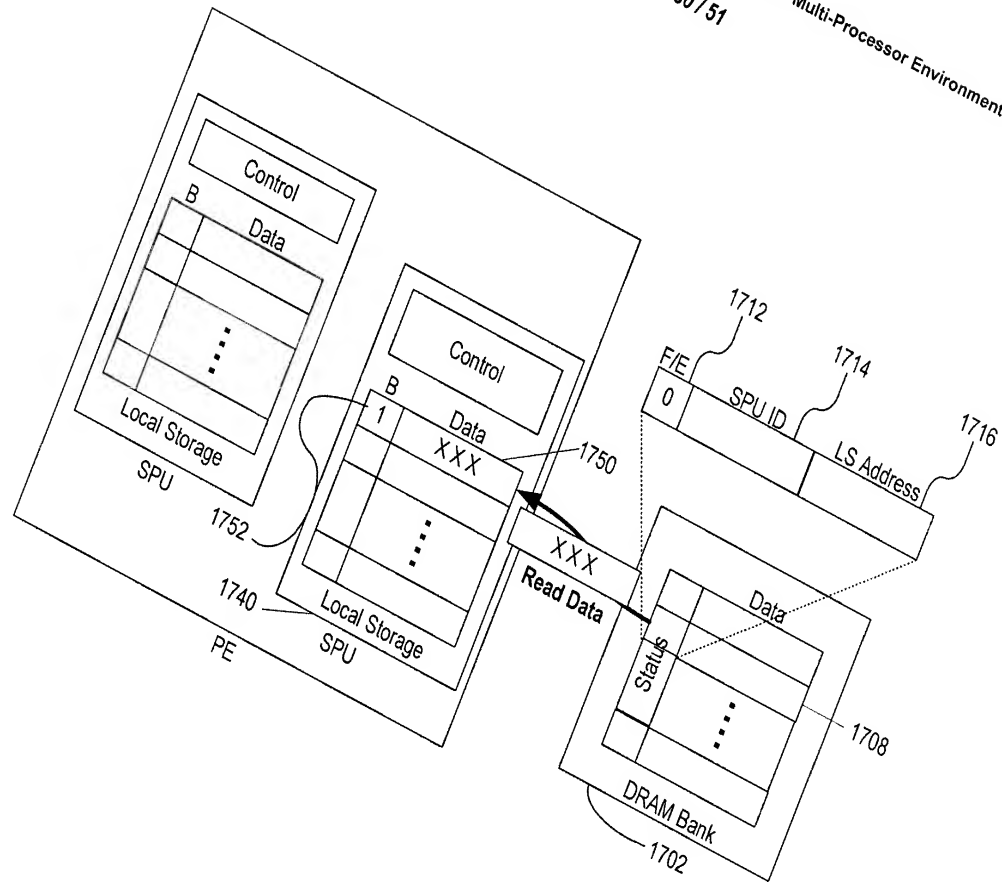
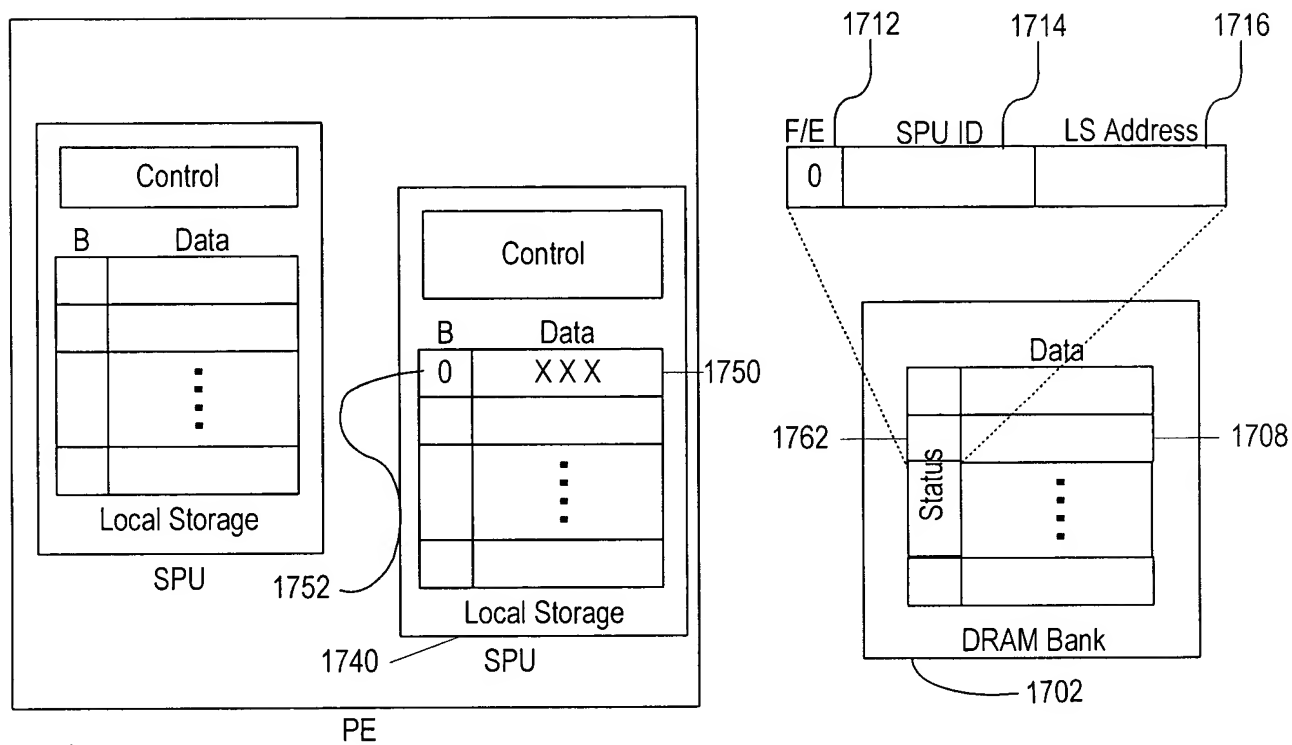
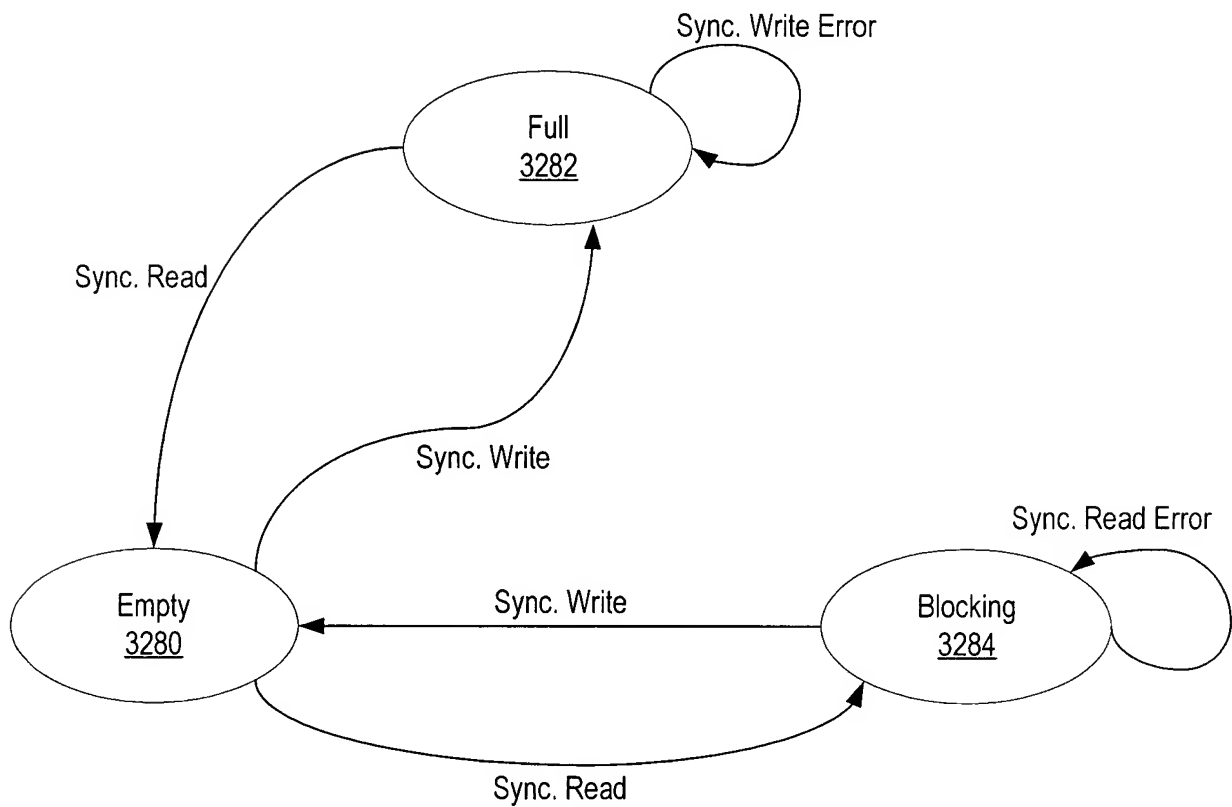


Figure 30

**Figure 31**

**Figure 32**

Key Control Table

3304 — ID 3306 — SPU Key 3308 — Key Mask

3302

0	SPU Key	Key Mask
1	SPU Key	Key Mask
2	SPU Key	Key Mask
		⋮
7	SPU Key	Key Mask

Figure 33

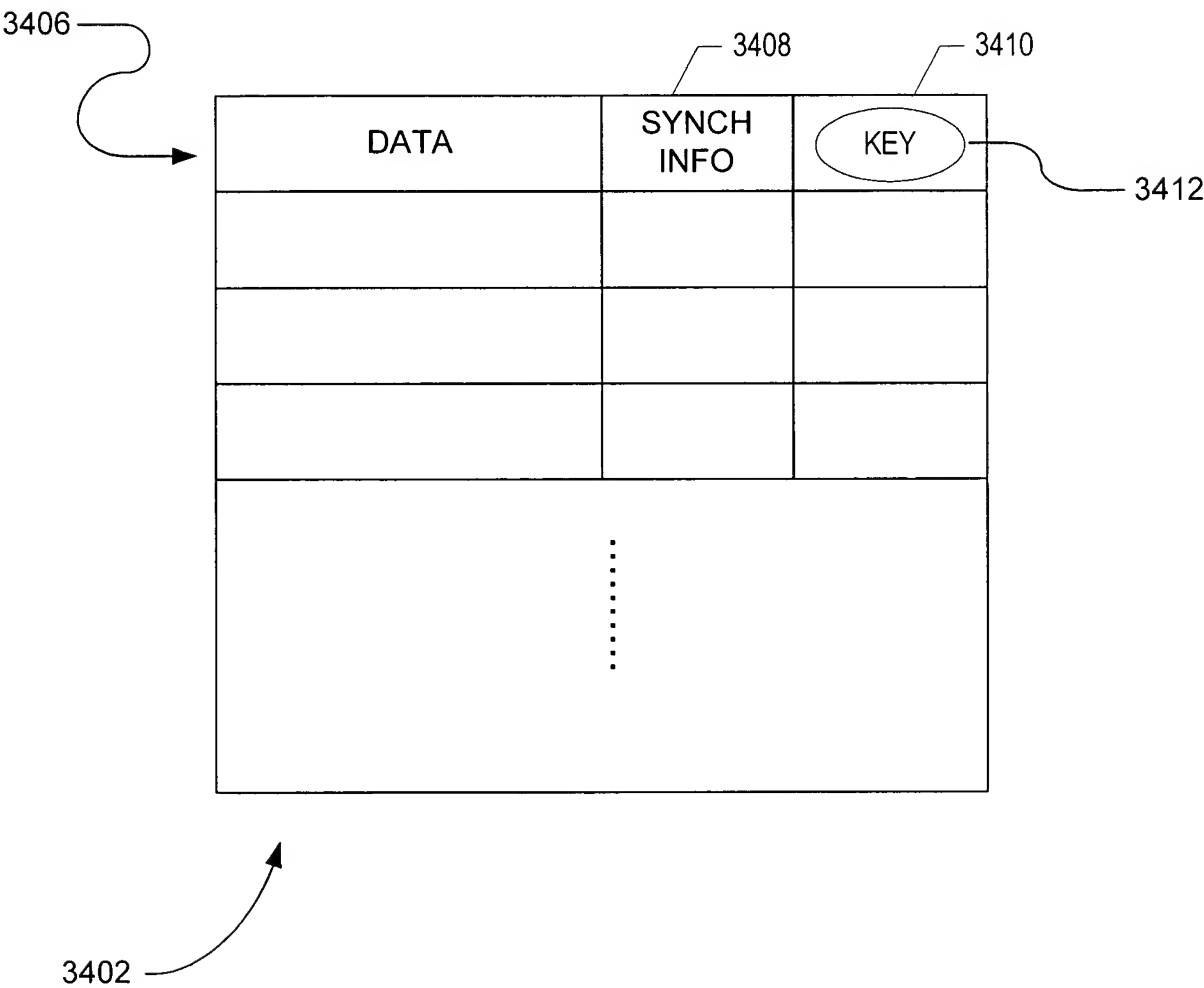


Figure 34

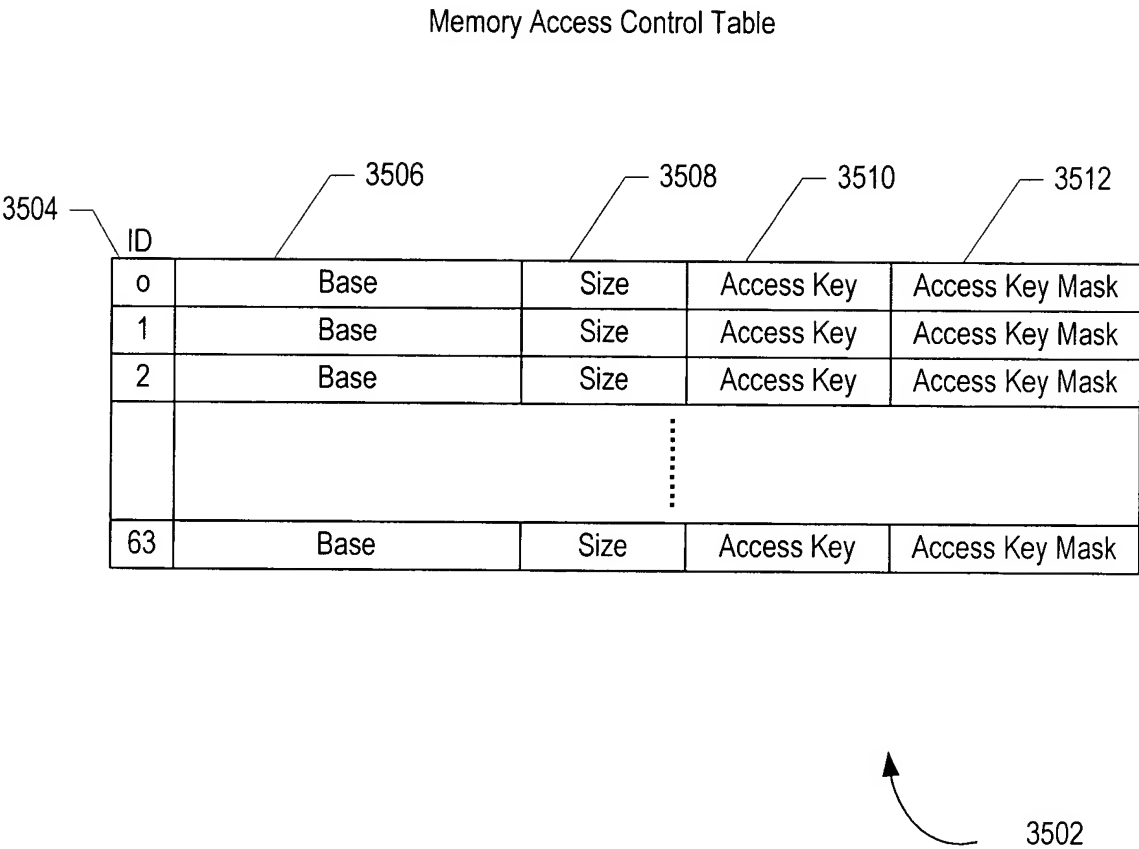
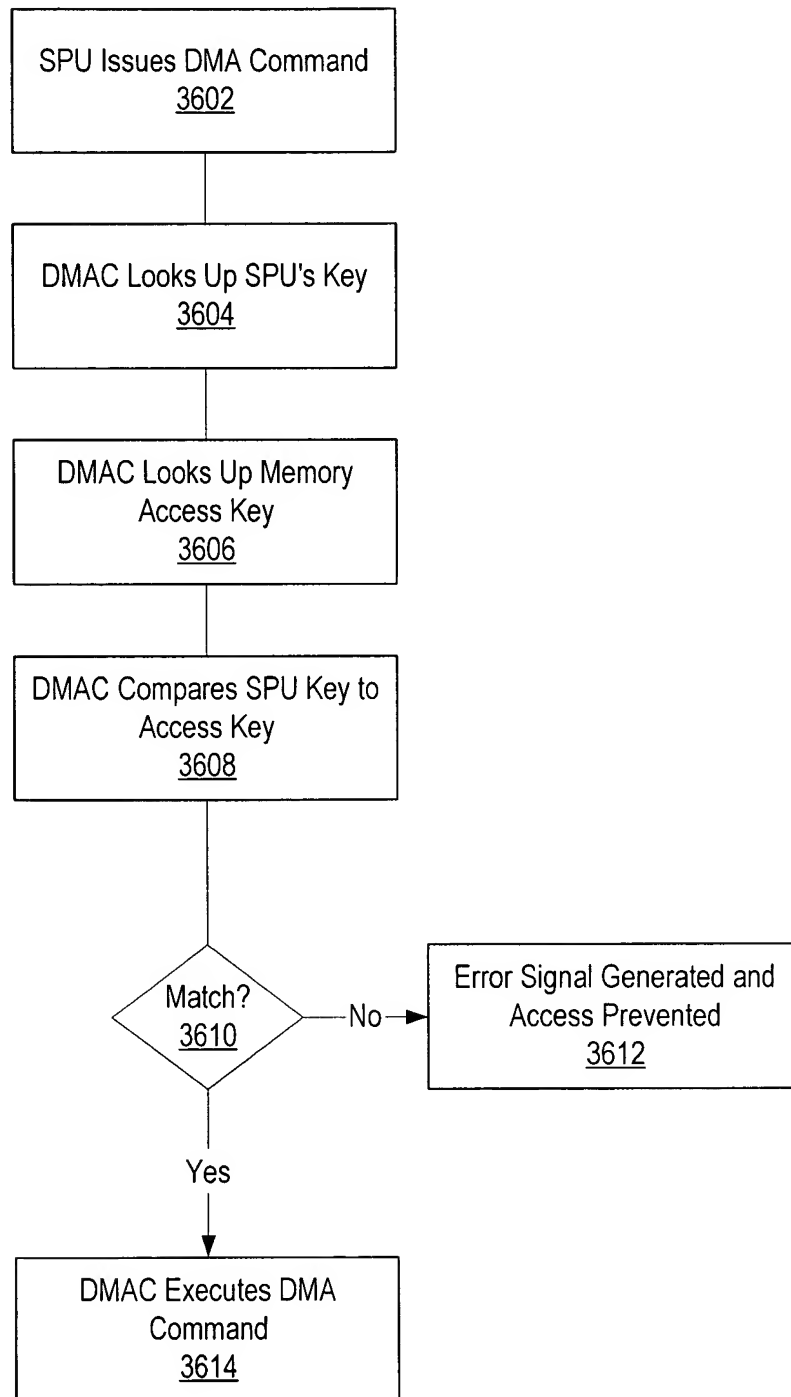
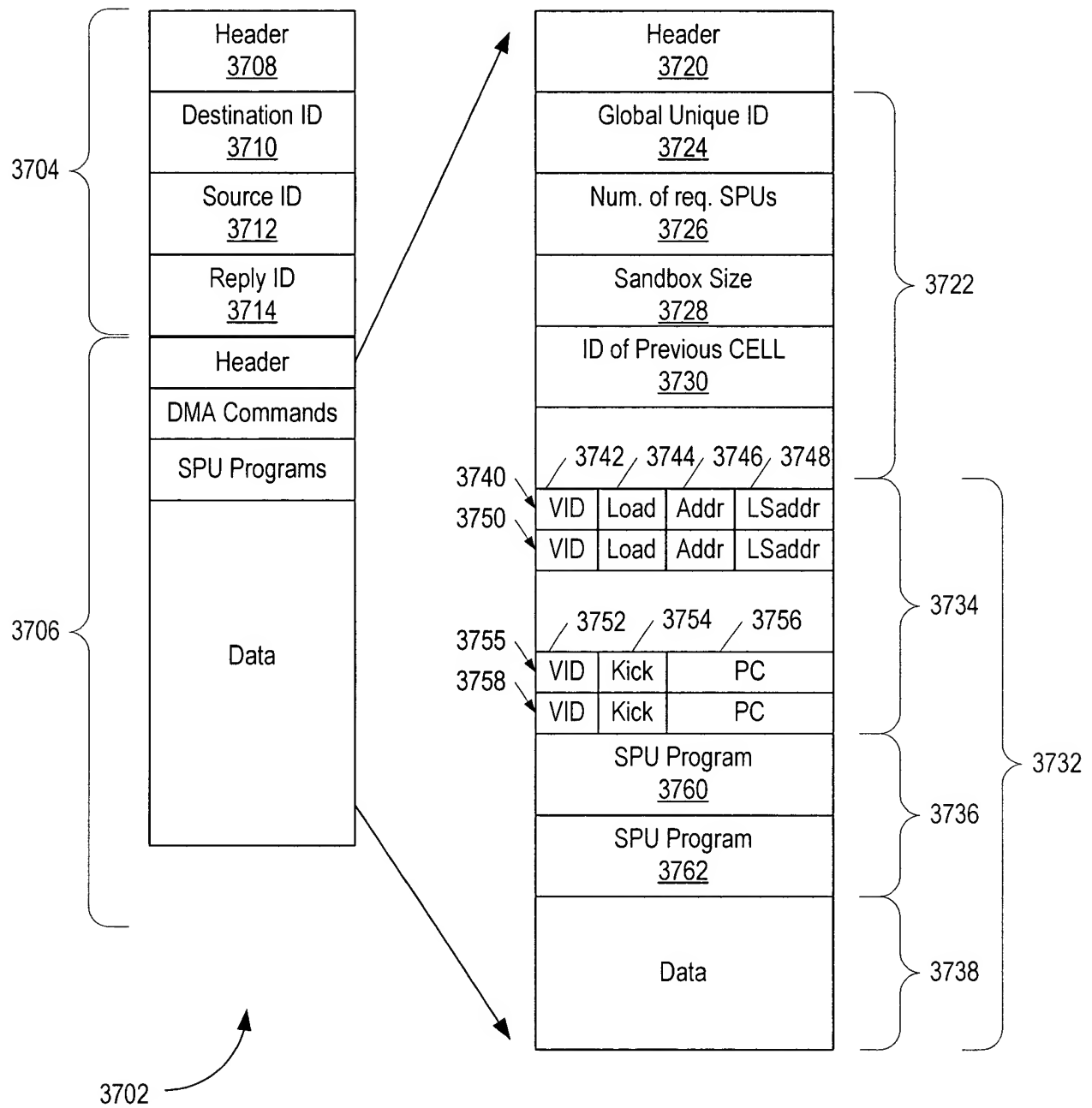


Figure 35

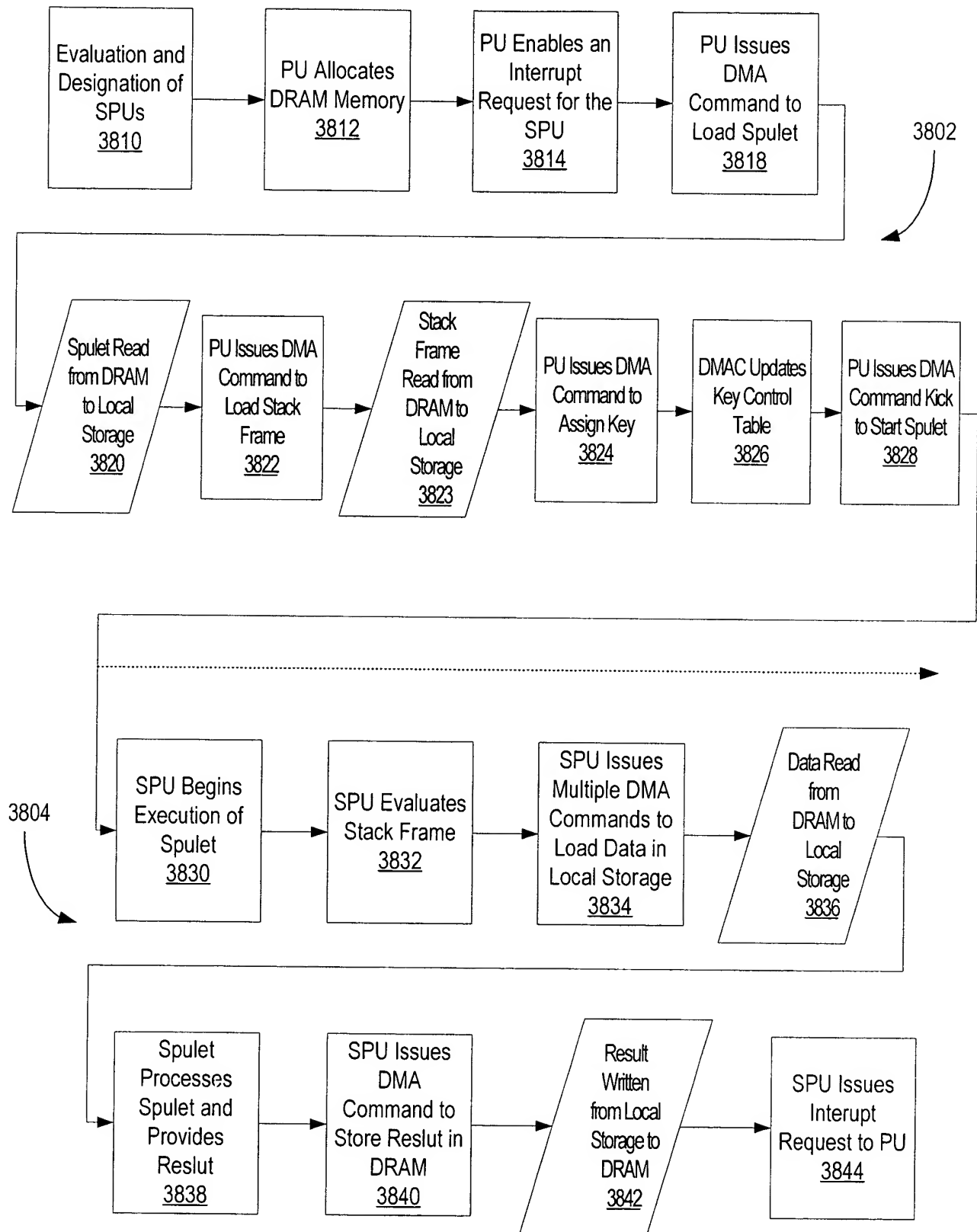
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**Figure 36**

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**Figure 38**

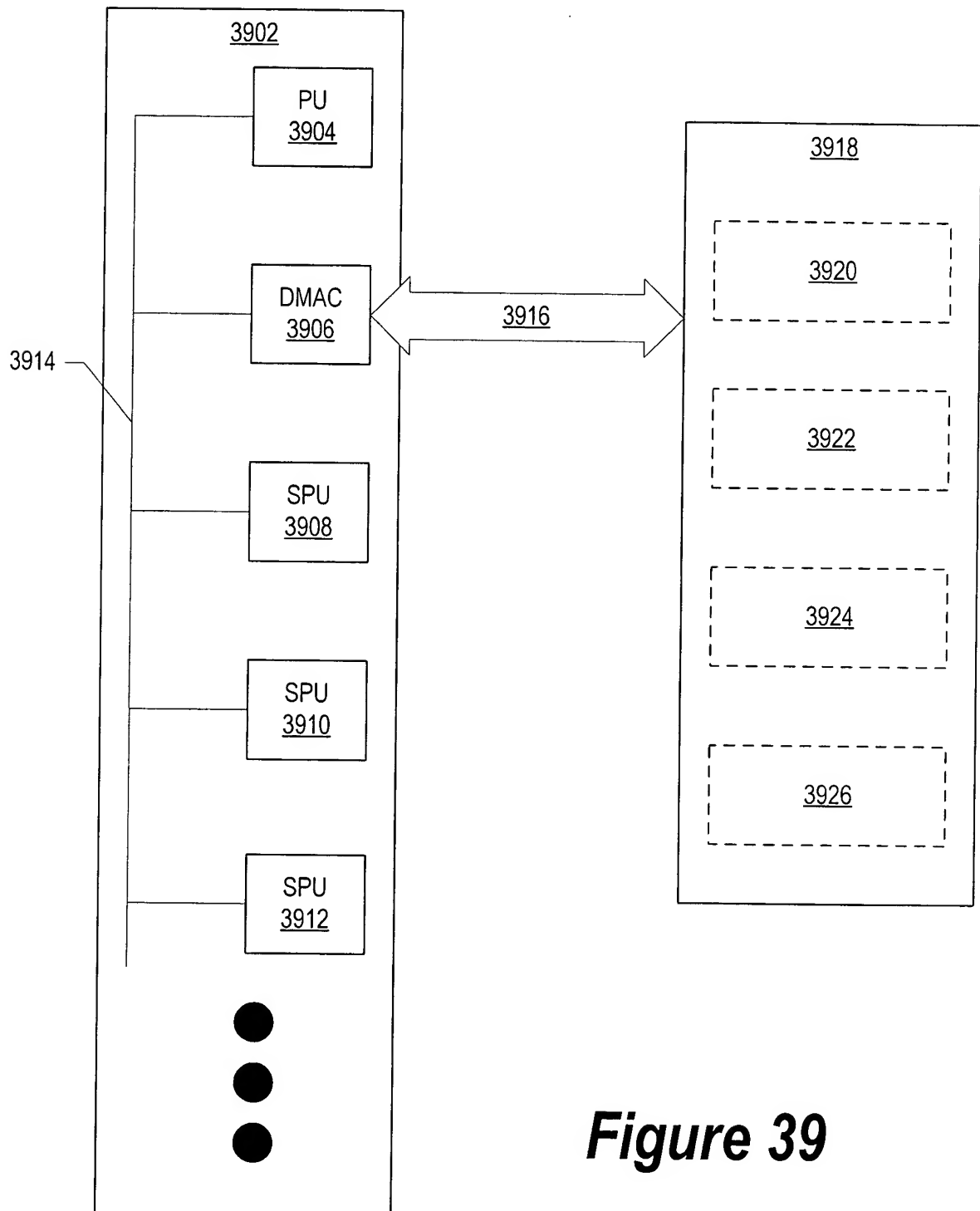
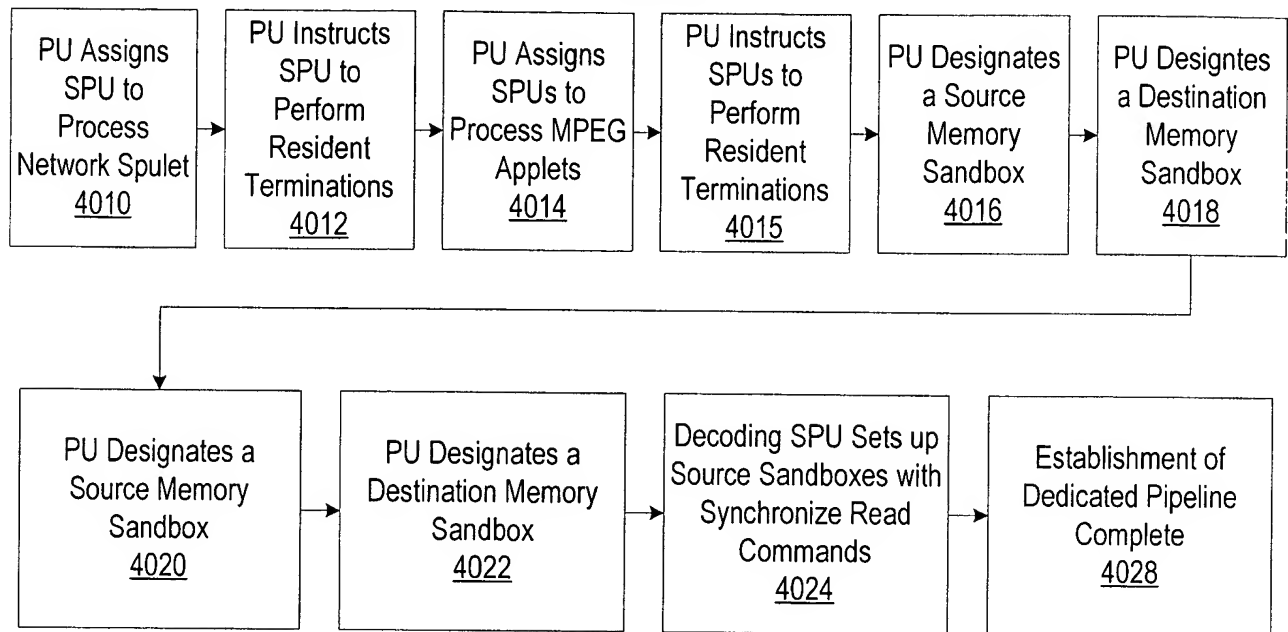
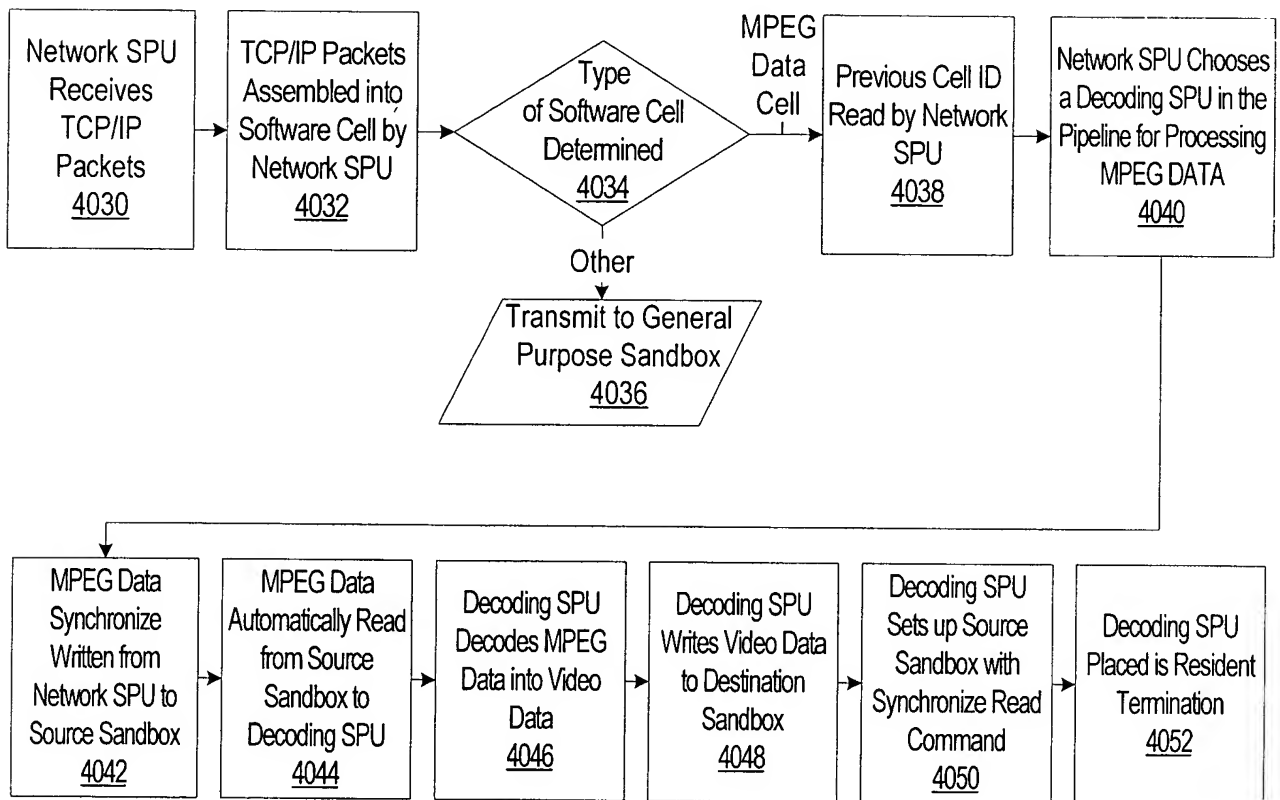


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**Figure 40A****Figure 40B**

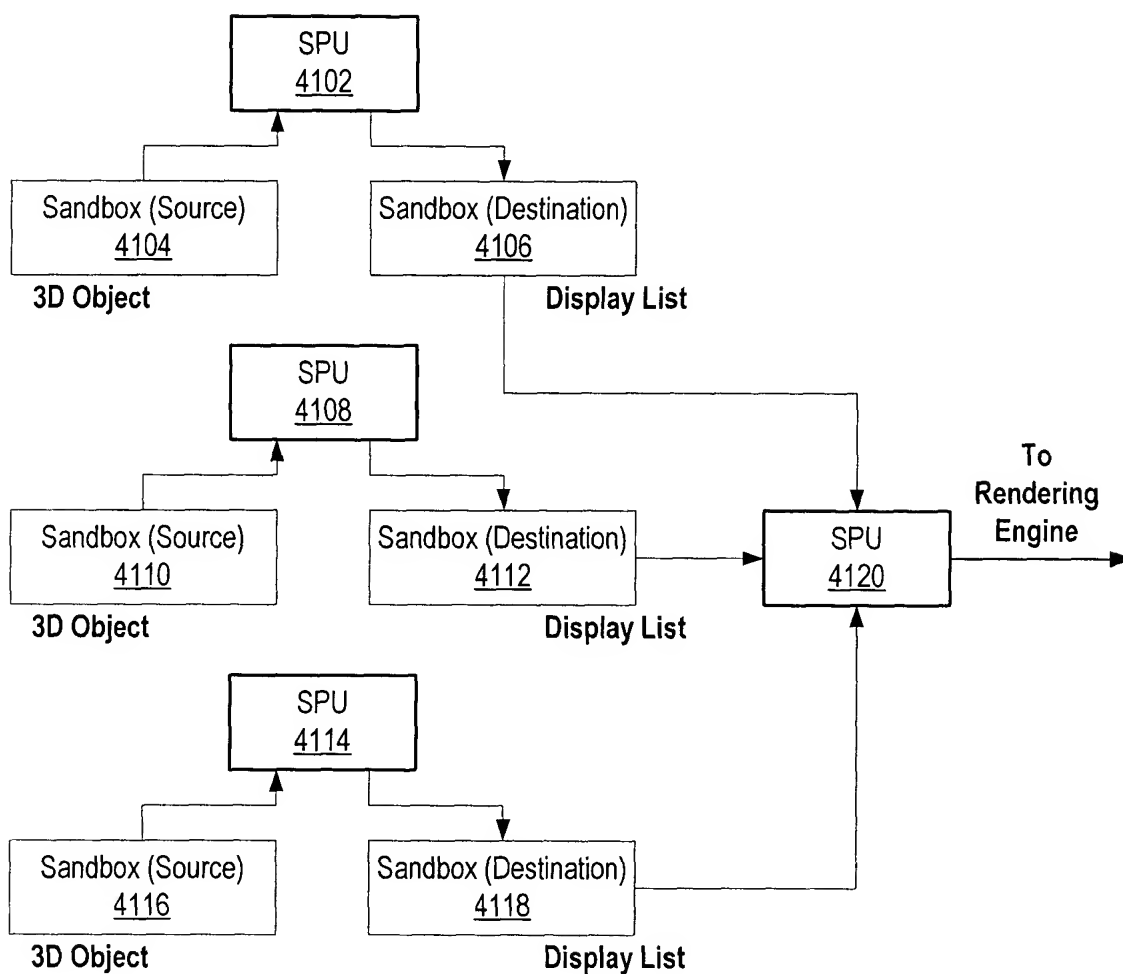


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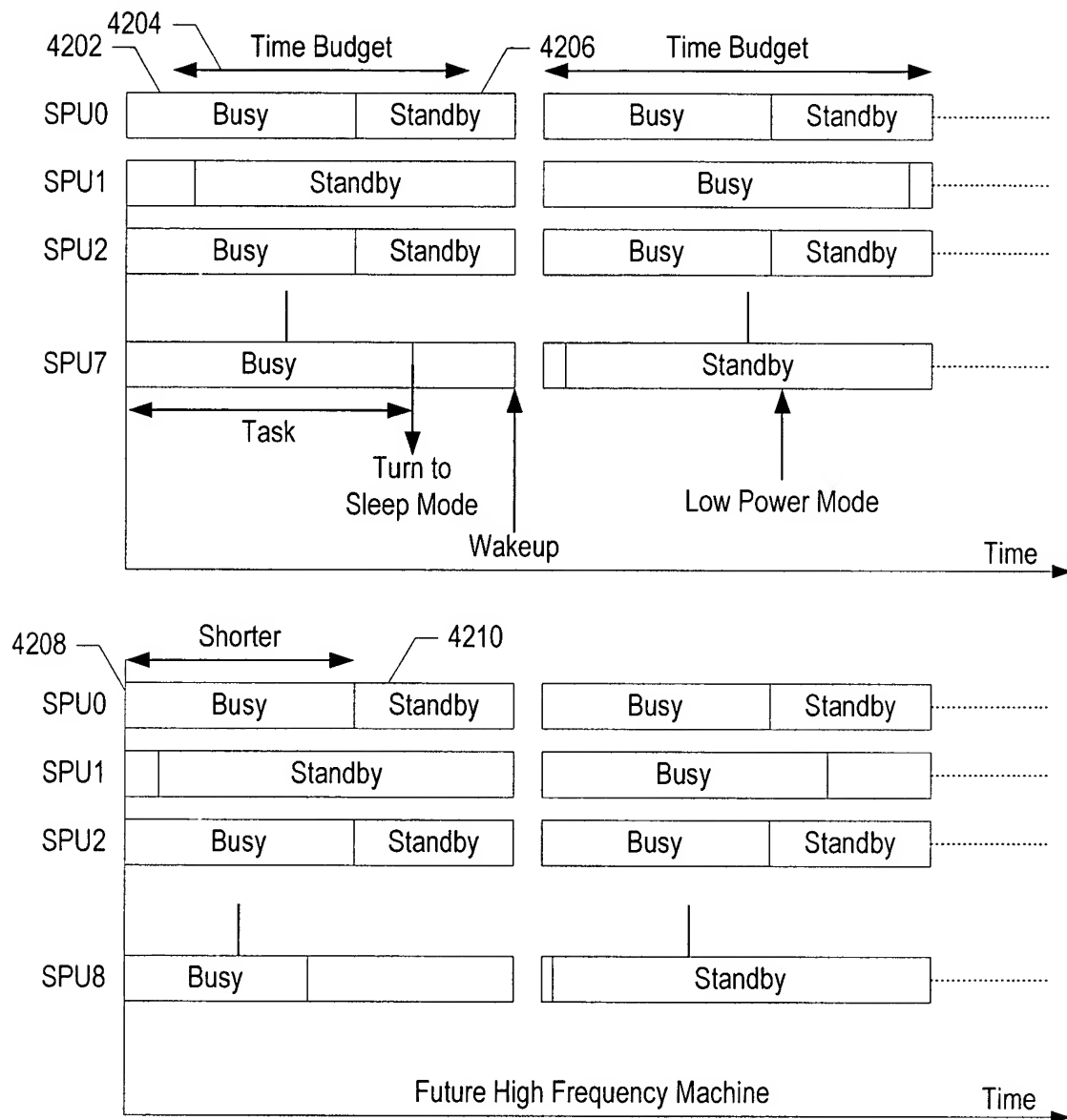
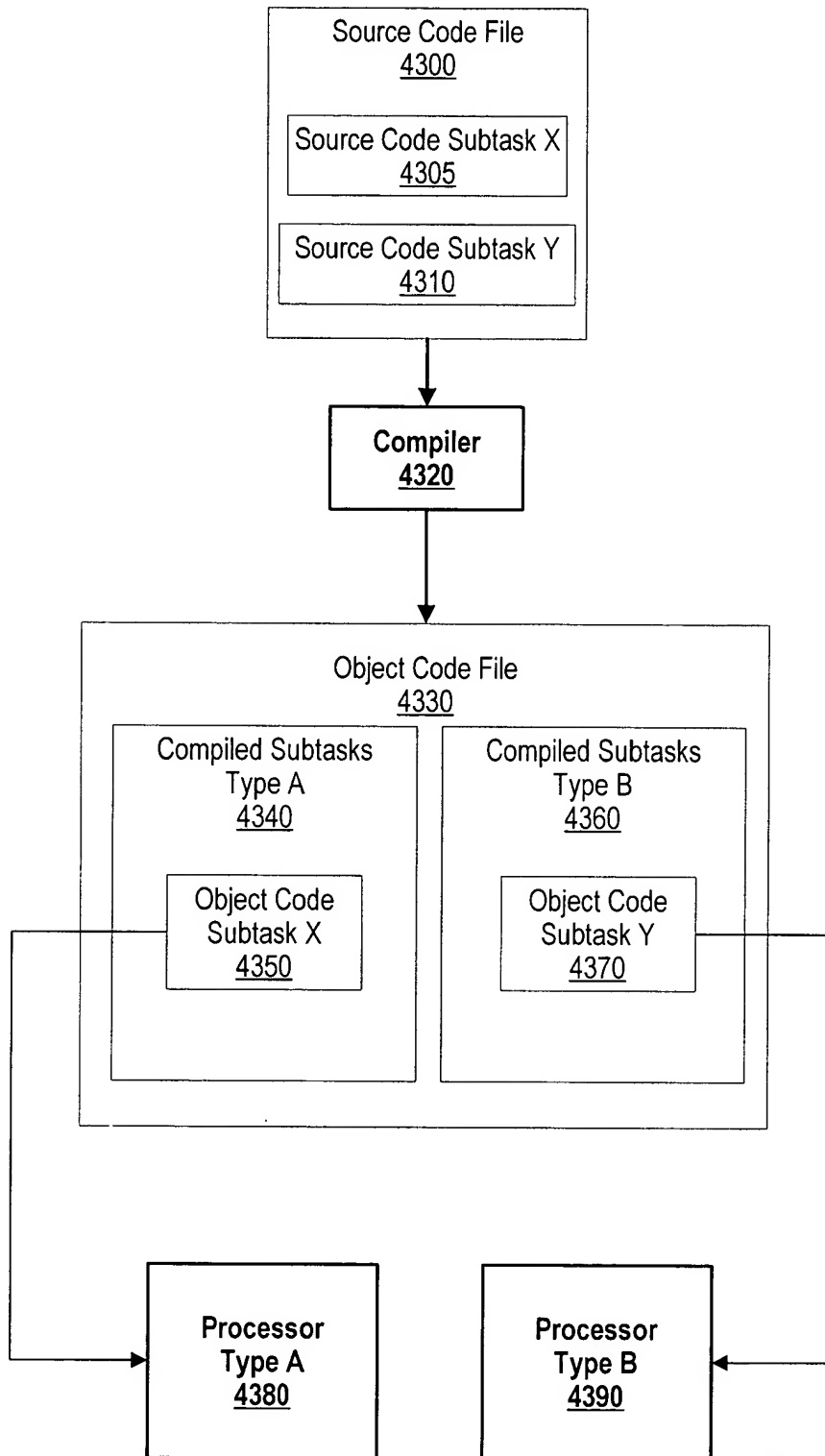
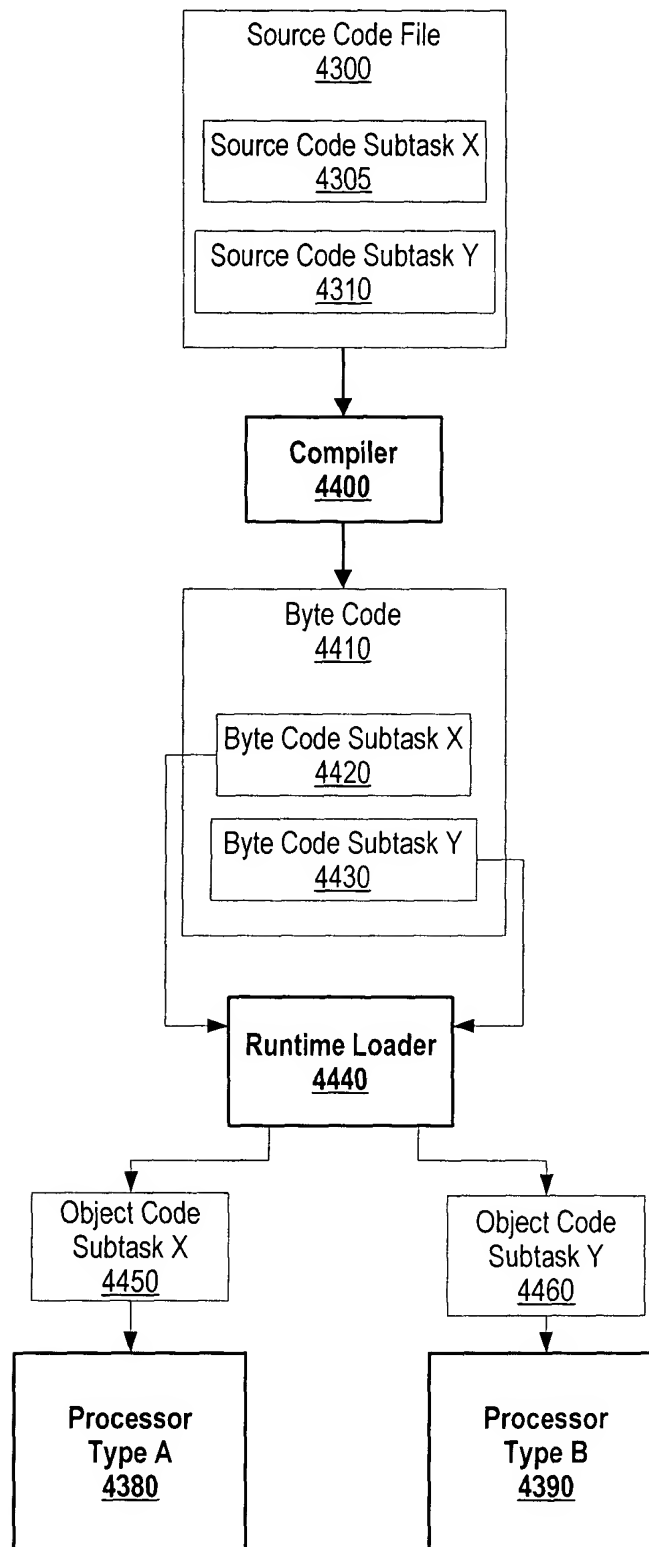


Figure 42

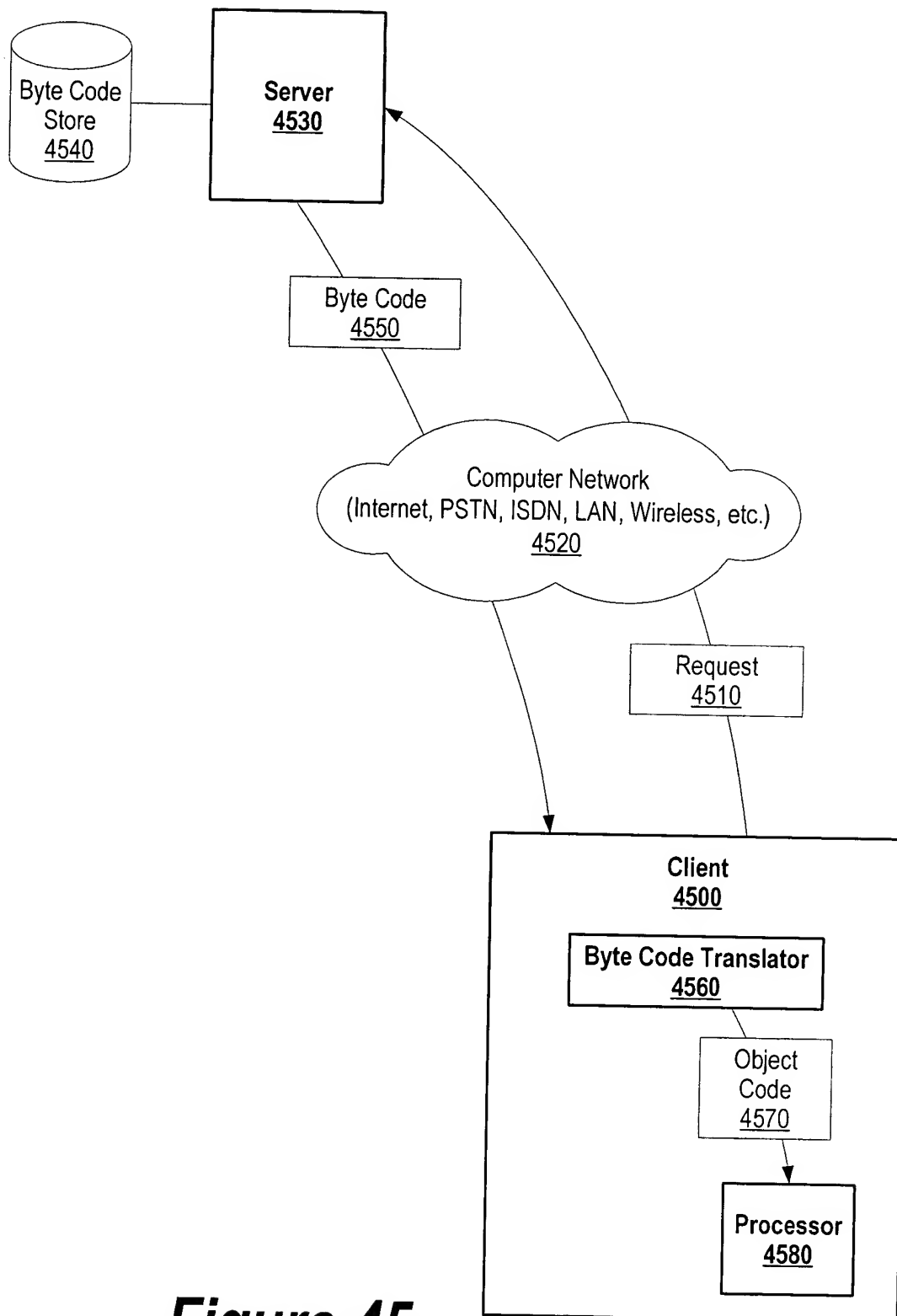
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**Figure 43**

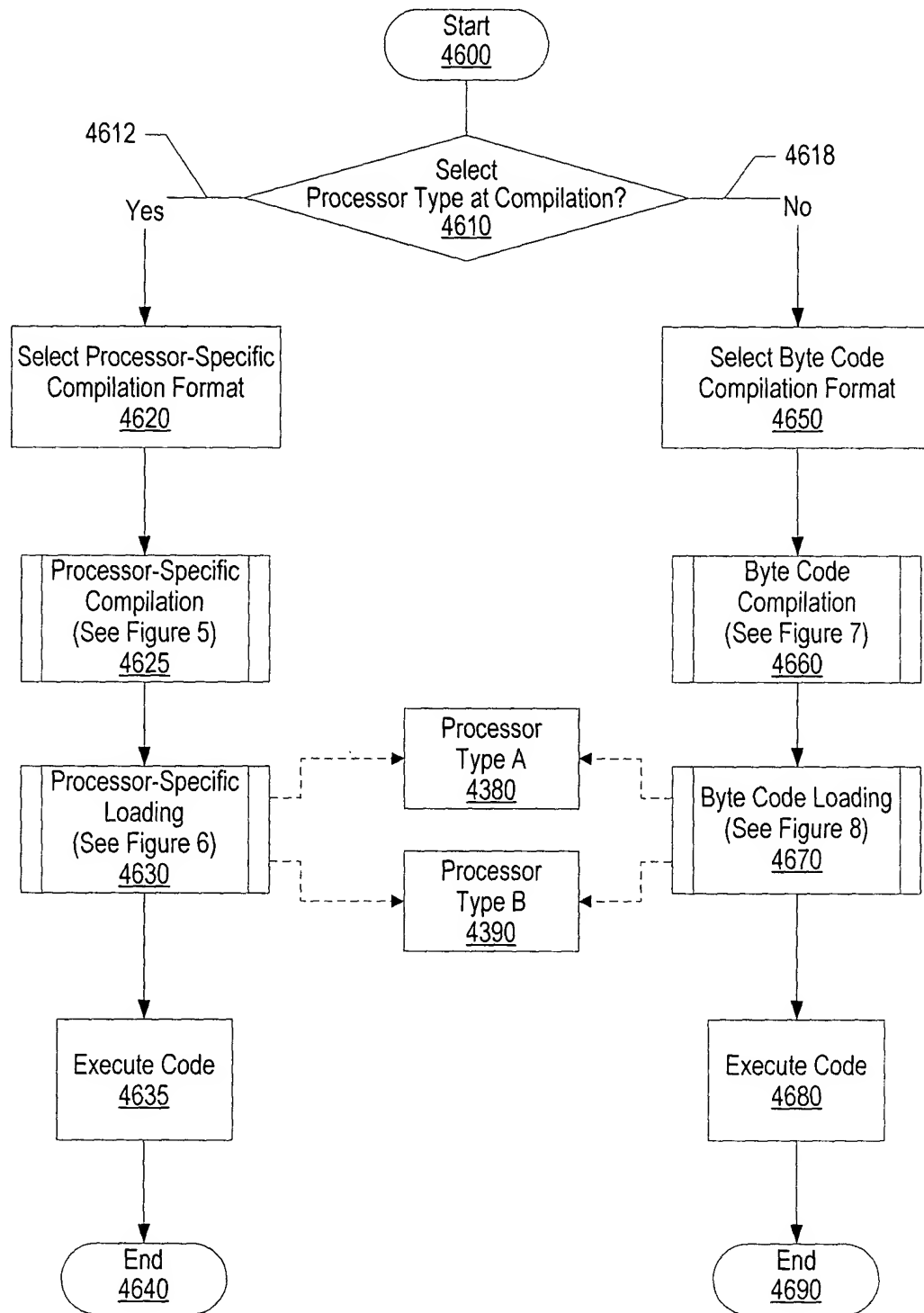
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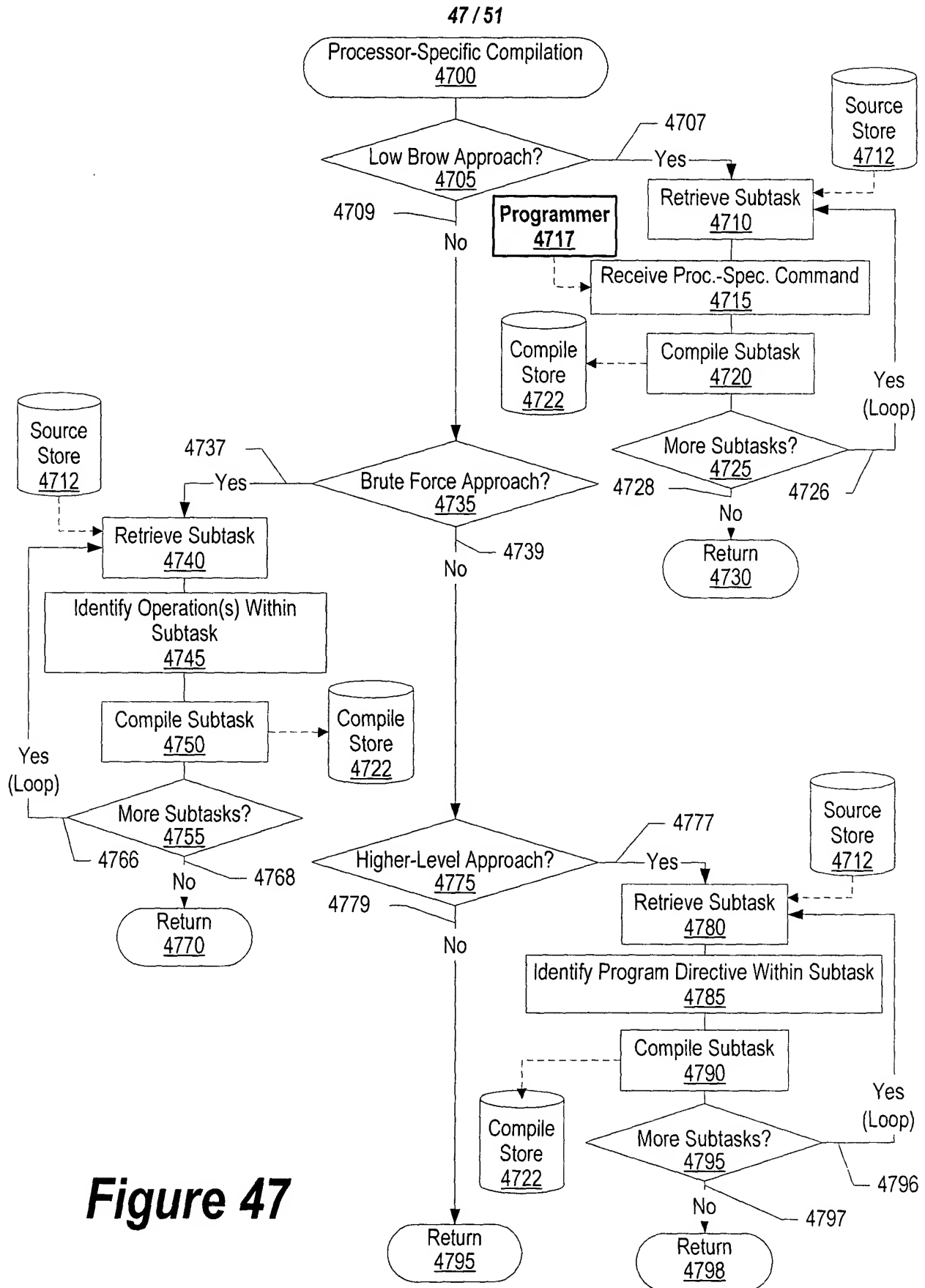
**Figure 44**

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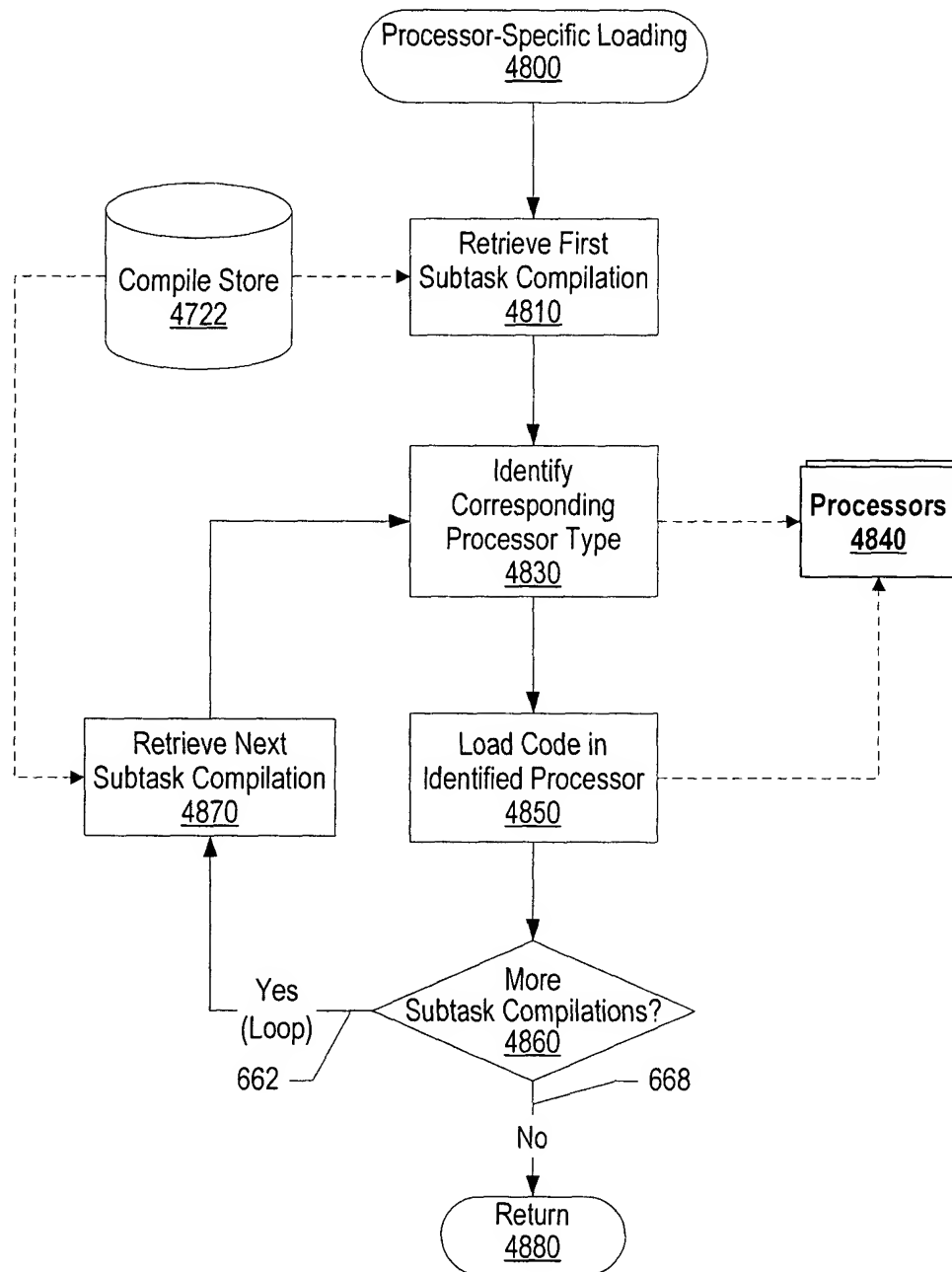
**Figure 45**

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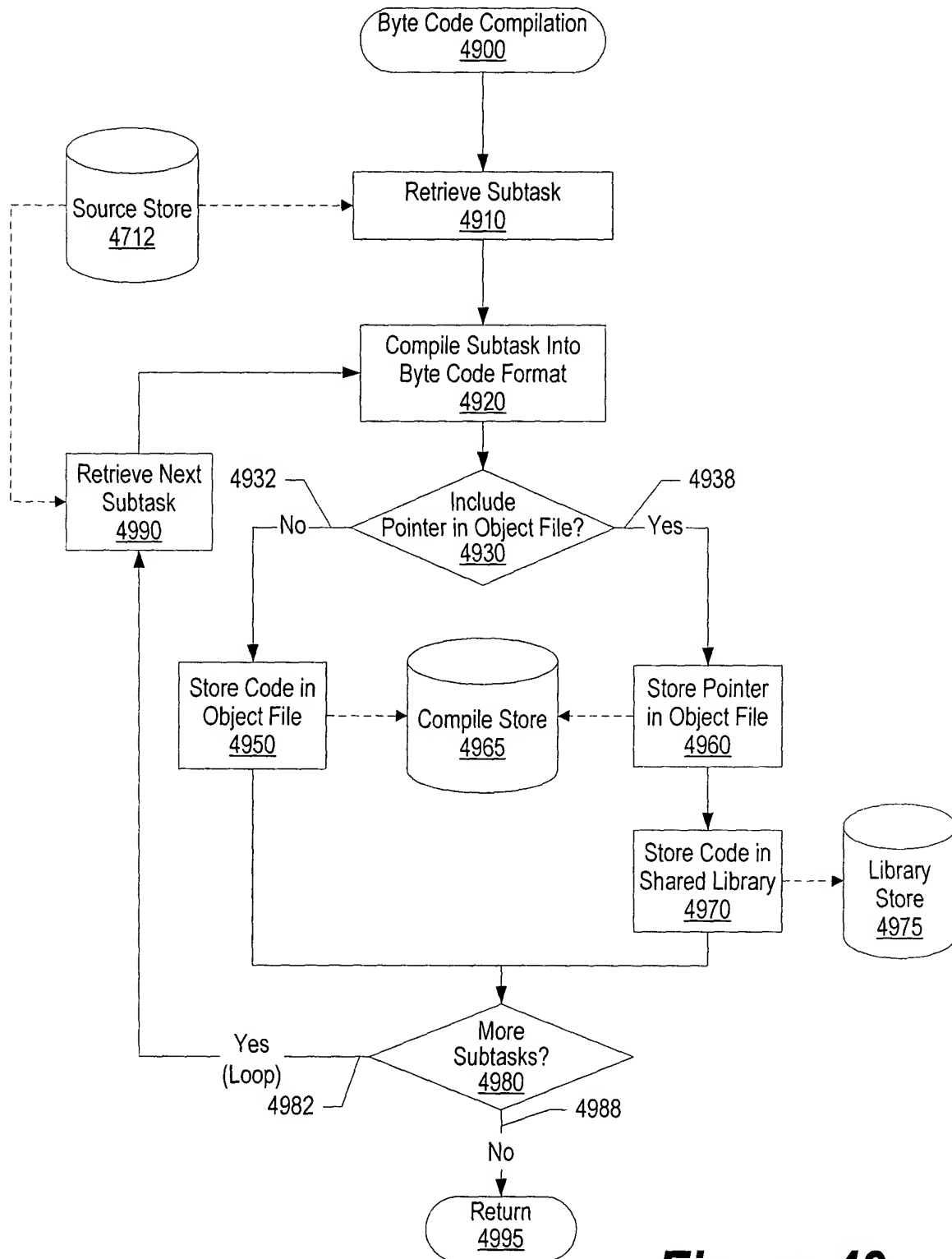
**Figure 46**

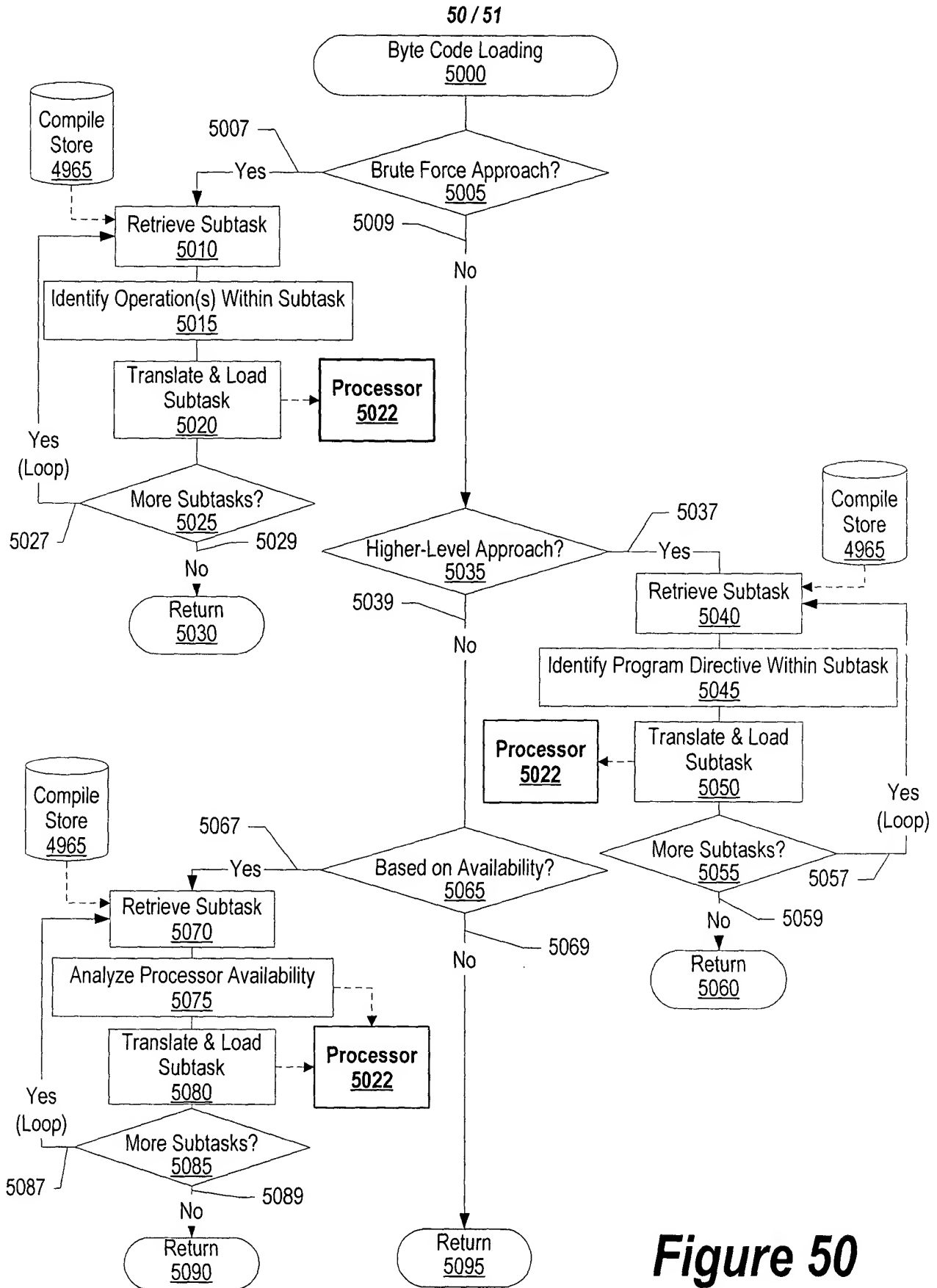


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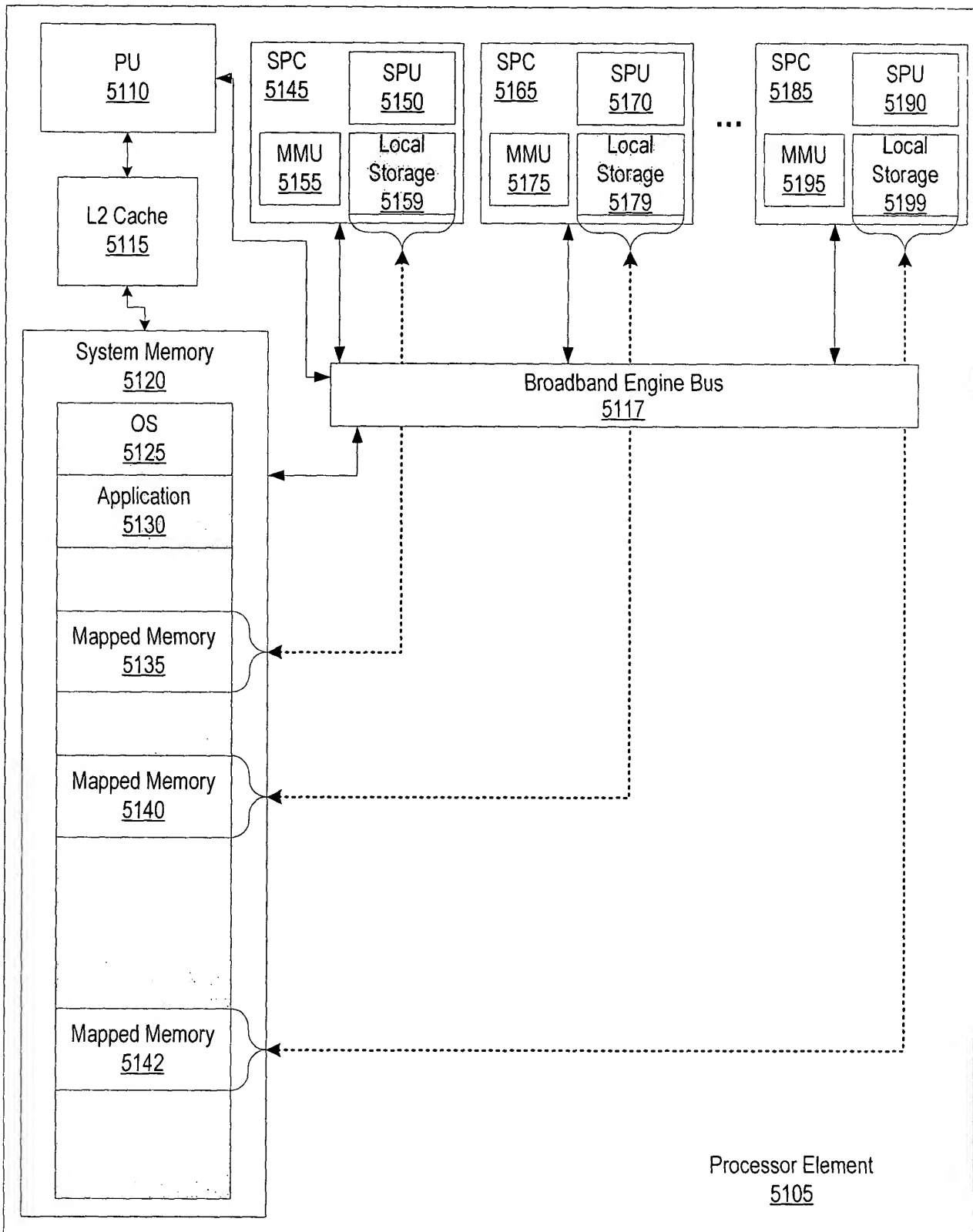
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**Figure 49**



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**Figure 51**